40V, 10mΩ, 10A, Single N-Channel

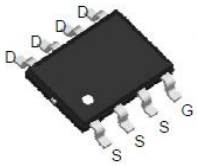
1.Features

- 40V MOSFET technology
- Low on-state resistance
- Fast switching
- Vgs±20V

V_{DS}	$R_{DS(on)}$ Typ.	I_D Max.
40V	10mΩ @ 10V	10A
	13mΩ @ 6V	
	20mΩ @ 4.5V	

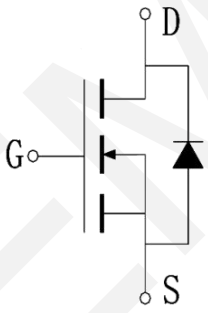
2.Applications

- Power Switching Application
- Load Switching



SOP8

Pin Description



Schematic Diagram

3.Absolute Max Ratings at Ta=25℃ (Note1)

Parameter	Symbol	Maximum	Units
Drain to Source Voltage	V_{DSS}	40	V
Gate to Source Voltage	V_{GSS}	±20	V
Drain Current (DC)	I_D	10	A
Drain Current (Pulse), PW≤300μs	I_{DP}	120	A
Total Dissipation	P_D	1.7	W
Avalanche Energy, Single Pulsed	E_{AS}	64	mJ
Junction Temperature	T_j	150	℃
Storage Temperature	T_{stg}	-55 to +150	℃

Note 1: Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

4.Thermal Resistance Ratings

Parameter	Symbol	Value	Unit
Junction-to-Ambient	$R_{\theta JA}$	31	℃/W

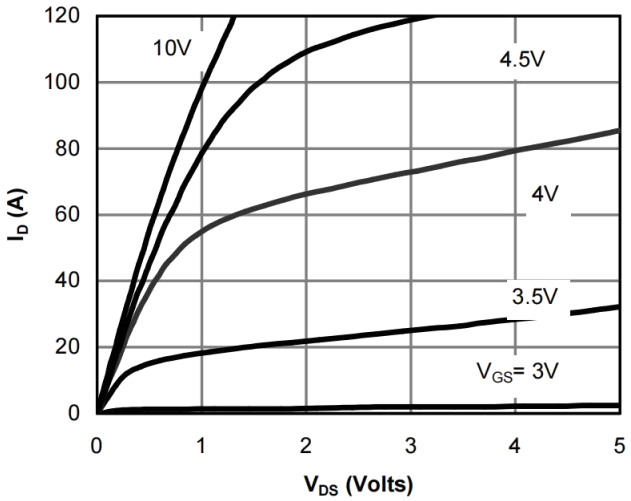
Note 2： When mounted on 1 inch square copper board t ≤ 10sec The value in any given application depends on the user's specific board design.

5.Electrical Characteristics at Ta=25℃ (Note 3)

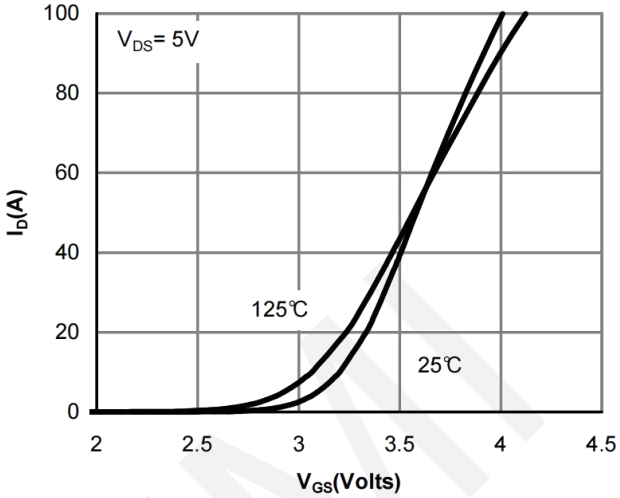
Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Units
Drain to Source Breakdown Voltage	$V_{(BR)DSS}$	I_D = 250μA, V_{GS} = 0V	40	45		V
Zero-Gate Voltage Drain Current	I_{DSS}	V_{DS} = 40V, V_{GS} = 0V			1	μA
Gate to Source Leakage Current	I_{GSS}	V_{GS} = ±20V, V_{DS} = 0V			±100	nA
On state drain current	$I_{D(ON)}$	V_{GS} = 10V, V_{DS} = 5V	120			A
Gate Threshold Voltage	$V_{GS(th)}$	V_{DS}=V_{GS}, I_{DS}=250μA	1	1.6	2.5	V
Static Drain to Source On-State Resistance	$R_{DS(on)}$	I_D = 10A, V_{GS} = 10V	-	10	13	mΩ
		I_D = 8A, V_{GS} = 6V	-	13	19	mΩ
		I_D = 8A, V_{GS} = 4.5V	-	20		mΩ
Input Capacitance	C_{iss}	V_{GS}=0V, V_{DS}=20V, Frequency=1.0MHz		1500		pF
Output Capacitance	C_{oss}			215		pF
Reverse Transfer Capacitance	C_{rss}			135		pF
Turn-ON Delay Time	$t_{d(on)}$	V_{DD} = 20V, R_L = 2Ω V_{GS} = 10V, R_G = 3Ω		6.4		ns
Rise Time	t_r			17		ns
Turn-OFF Delay Time	$t_{d(off)}$			30		ns
Fall Time	t_f			16.8		ns
Total Gate Charge	Q_g	V_{DS} = 20V, V_{GS} = 10V, I_D = 10A		27.2		nC
	Q_{gs}			4.5		nC
	Q_{gd}			6.4		nC
Diode Forward Voltage	V_{FSD}	I_S = 10A, V_{GS} = 0		0.8	1.2	V

Note 3： Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

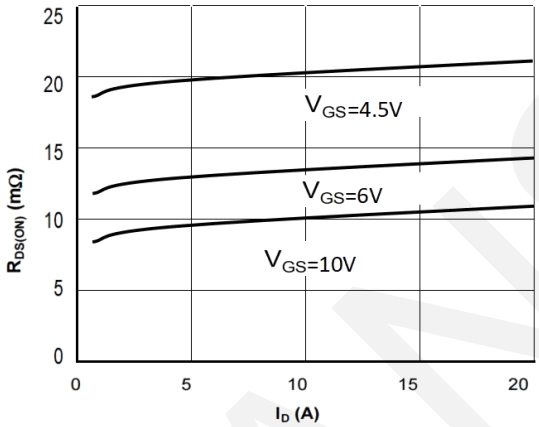
6.Typical electrical and thermal characteristics



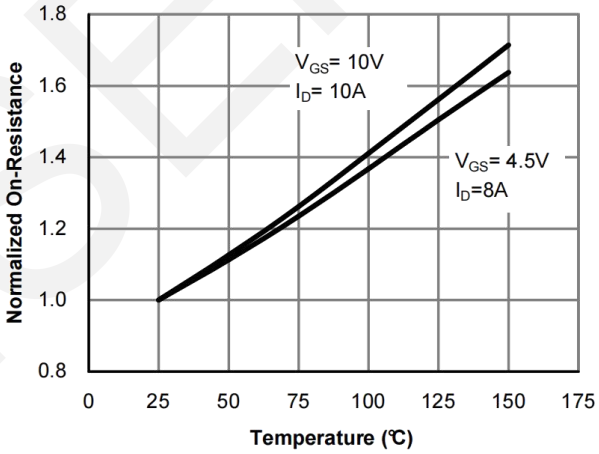
On-Region Characteristics



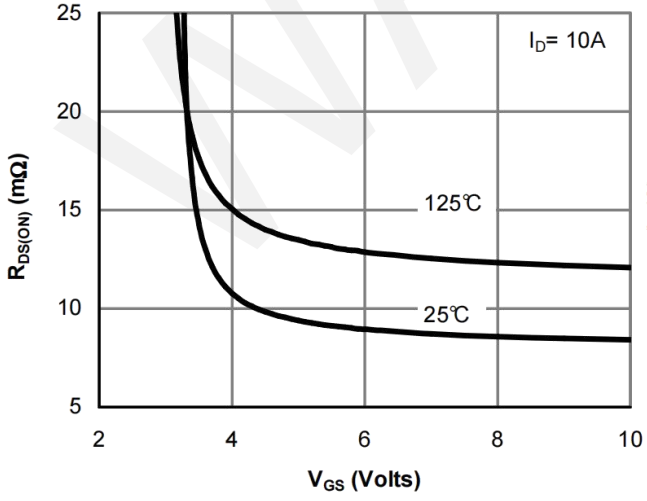
Transfer Characteristics



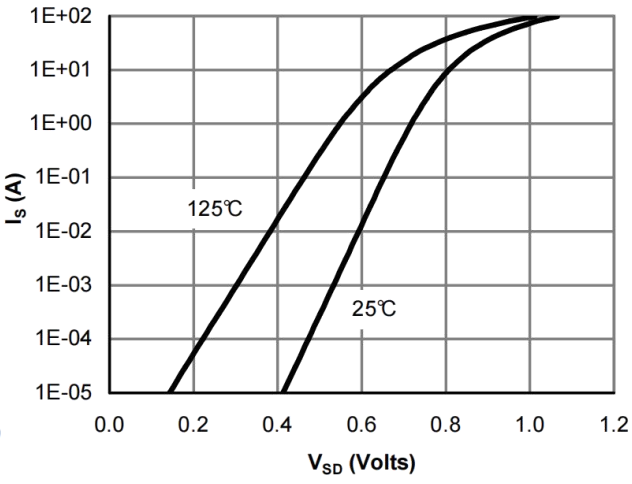
Rdson-Drain Current



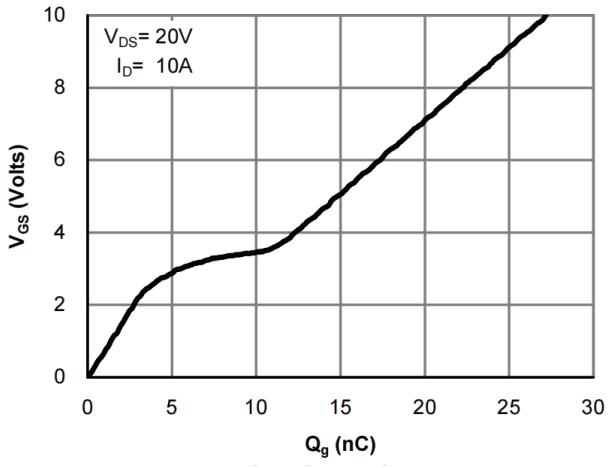
Rdson-Junction



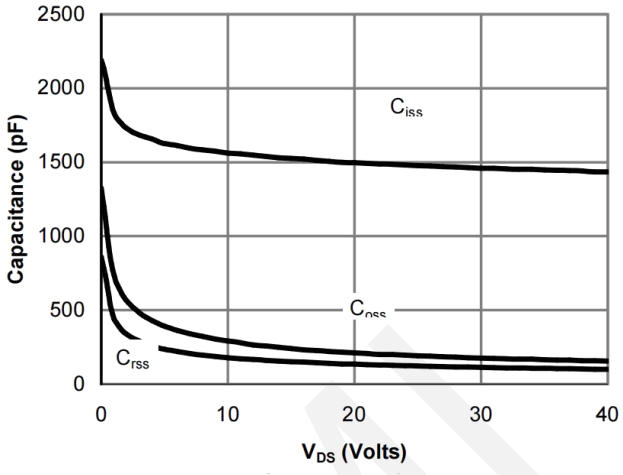
On-Resistance vs. Gate-Source Voltage



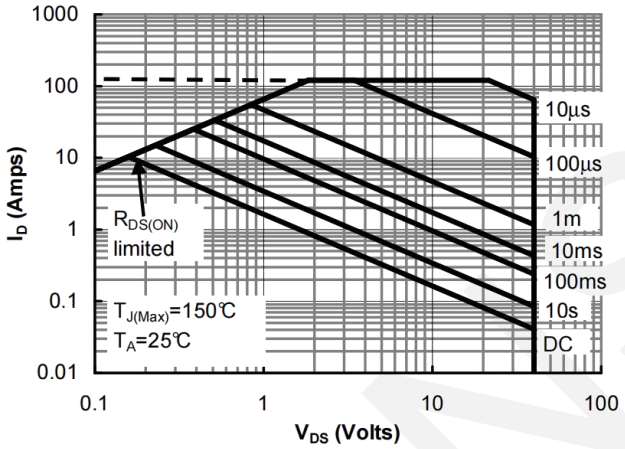
Body-Diode Characteristics



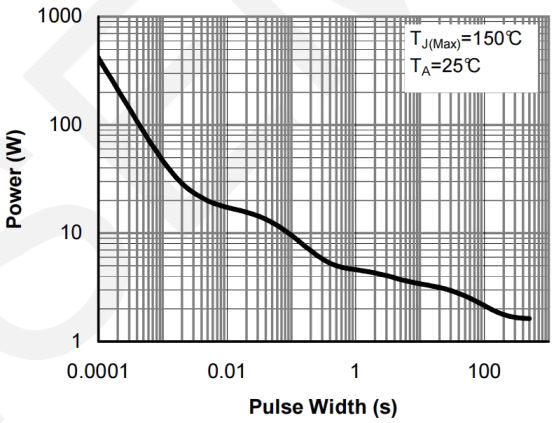
Gate-Charge Characteristics



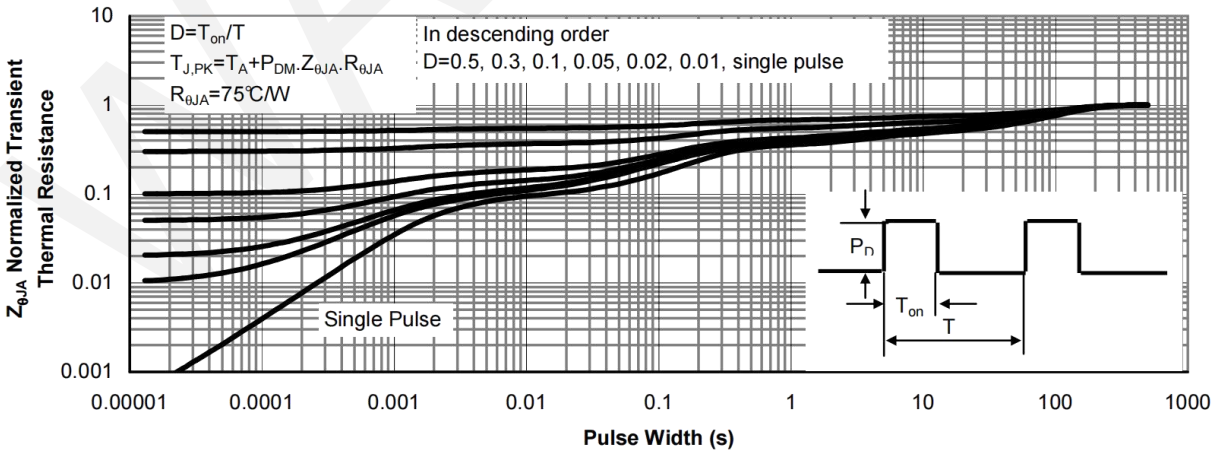
Capacitance CharacteristicsArea



Maximum Forward Biased Safe Operating Area

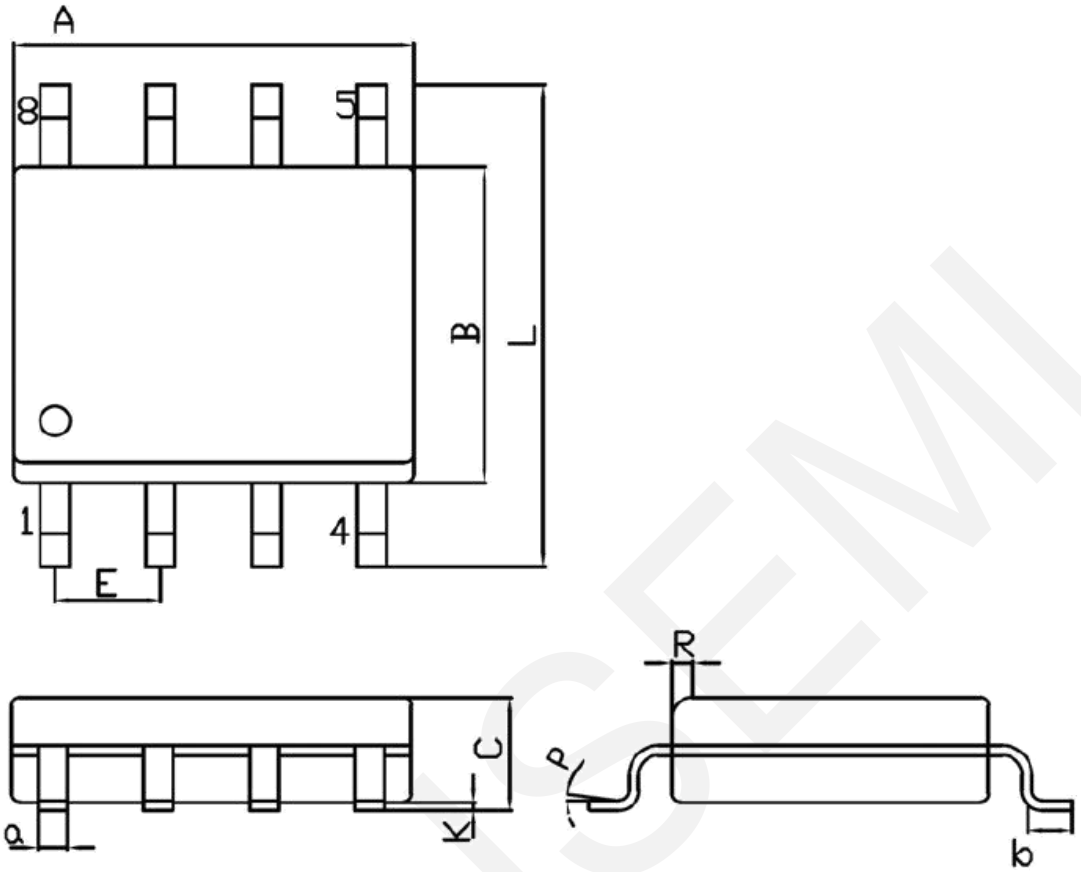


Single Pulse Power Rating Junction-to-Ambient



Normalized Maximum Transient Thermal Impedance

7.Package Dimensions



Symbol	Dimensions In Millimeters		Symbol	Dimensions In Millimeters	
	Min	Max		Min	Max
A	4.70	5.10	C	1.35	1.75
B	3.70	4.10	a	0.35	0.49
L	5.80	6.20	R	0.30	0.60
E	1.27BSC		P	0°	7°
K	0.12	0.22	b	0.40	1.25

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