45V, 6mΩ, 80A, Single N-Channel

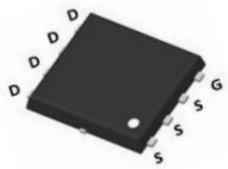
1.Features

- 45V MOSFET technology
- Low on-state resistance
- Fast switching
- Vgs±20V

V_{DS}	$R_{DS(on)}$ Typ.	I_D Max.
45V	6mΩ @ 10V	80A
	10.6mΩ @ 4.5V	

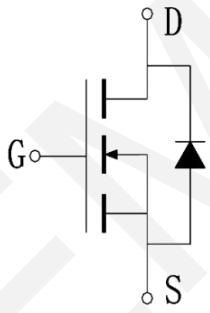
2.Applications

- Power Switching Application
- Load Switching



PDFN5x6

Pin Description



Schematic Diagram

3.Absolute Max Ratings at Ta=25℃ (Note1)

Parameter	Symbol	Maximum	Units
Drain to Source Voltage	V_{DSS}	45	V
Gate to Source Voltage	V_{GSS}	±20	V
Drain Current (DC)	I_D	80	A
Drain Current (Pulse), PW≤300μs	I_{DP}	216	A
Total Dissipation	P_D	80	W
Avalanche Energy, Single Pulsed	E_{AS}	113	mJ
Junction Temperature	T_j	150	℃
Storage Temperature	T_{stg}	-55 to +150	℃

Note 1: Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

4.Thermal Resistance Ratings

Parameter	Symbol	Value	Unit
Junction to case	$R_{\theta JC}$	1.8	℃/W

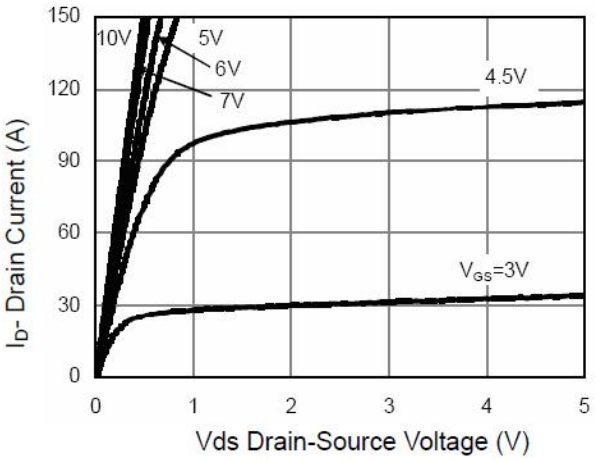
Note 2： When mounted on 1 inch square copper board t ≤ 10sec The value in any given application depends on the user's specific board design.

5.Electrical Characteristics at Ta=25℃ (Note 3)

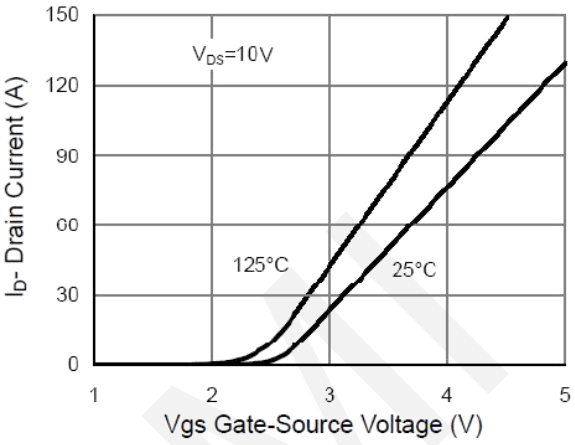
Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Units
Drain to Source Breakdown Voltage	$V_{(BR)DSS}$	I_D = 250μA, V_{GS} = 0V	45	52		V
Zero-Gate Voltage Drain Current	I_{DSS}	V_{DS} = 20V, V_{GS} = 0V			1	μA
Gate to Source Leakage Current	I_{GSS}	V_{GS} = ±12V, V_{DS} = 0V			±100	nA
Gate Threshold Voltage	$V_{GS(th)}$	V_{DS}=V_{GS}, I_{DS}=250μA	1	1.6	2.5	V
Static Drain to Source On-State Resistance	$R_{DS(on)}$	I_D = 20A, V_{GS} = 10V	-	6		mΩ
		I_D = 20A, V_{GS} = 4.5V	-	10.6		mΩ
Forward Transconductance	G_{FS}	I_D = 20A, V_{DS} = 10V	15			S
Input Capacitance	C_{iss}	V_{GS}=0V, V_{DS}=20V, Frequency=1.0MHz		2662		pF
Output Capacitance	C_{oss}			322		pF
Reverse Transfer Capacitance	C_{rss}			246		pF
Turn-ON Delay Time	$t_{d(on)}$	V_{DD} = 20V, R_L = 1Ω V_{GS} = 10V, R_G = 3Ω		12		ns
Rise Time	t_r			11		ns
Turn-OFF Delay Time	$t_{d(off)}$			39		ns
Fall Time	t_f			12		ns
Total Gate Charge	Q_g	V_{DS} = 20V, V_{GS} = 10V, I_D = 20A		54.3		nC
	Q_{gs}			6.9		nC
	Q_{gd}			14.5		nC
Diode Forward Voltage	V_{FSD}	I_S = 10A, V_{GS} = 0		0.9	1.2	V

Note 3： Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

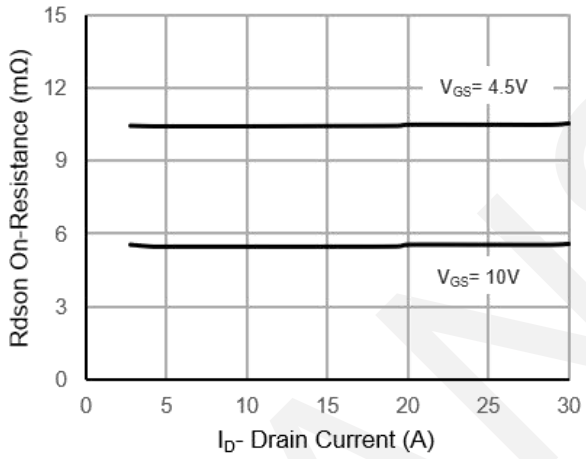
6.Typical electrical and thermal characteristics



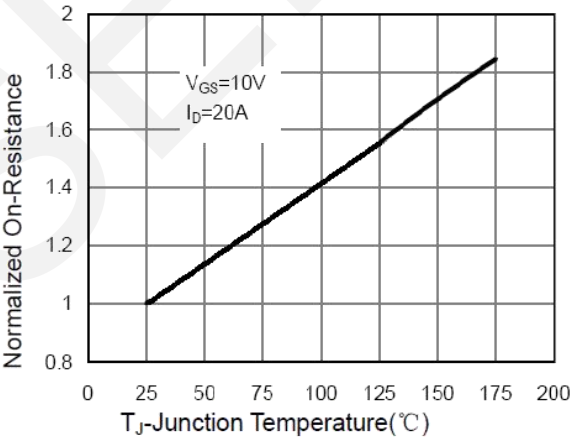
Output Characteristics



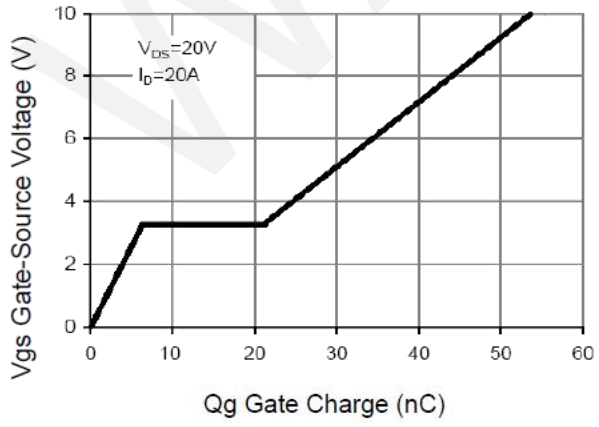
Transfer Characteristics



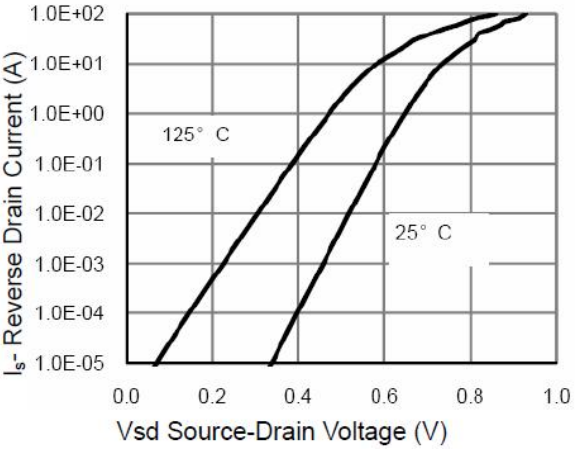
Rdson-Drain Current



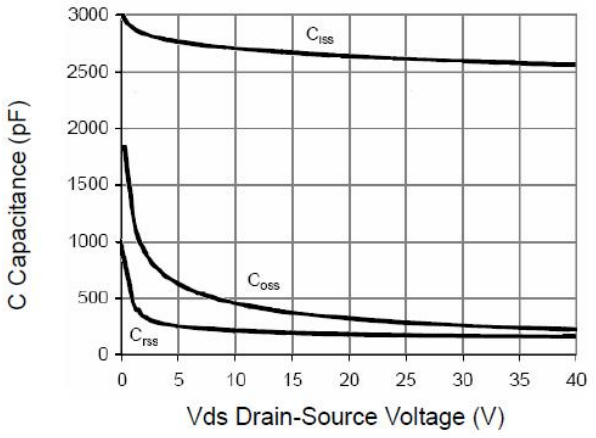
Rdson-Junction Temperature



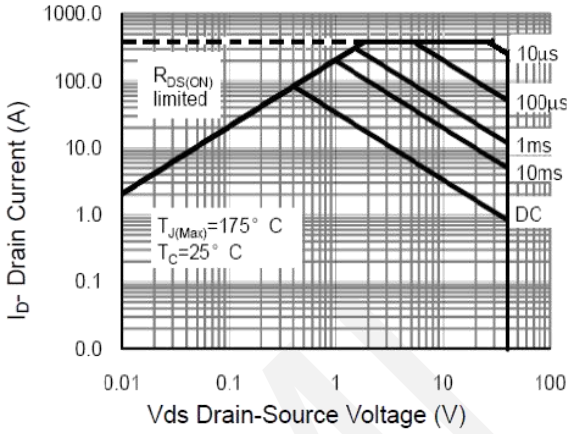
Gate Charge



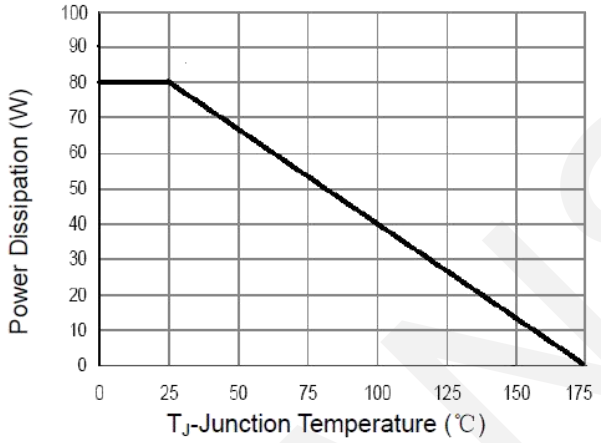
Source-Drain Diode Forward



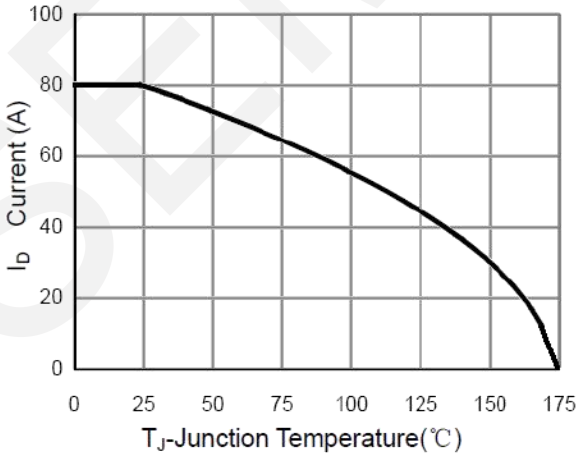
Capacitance vs Vds



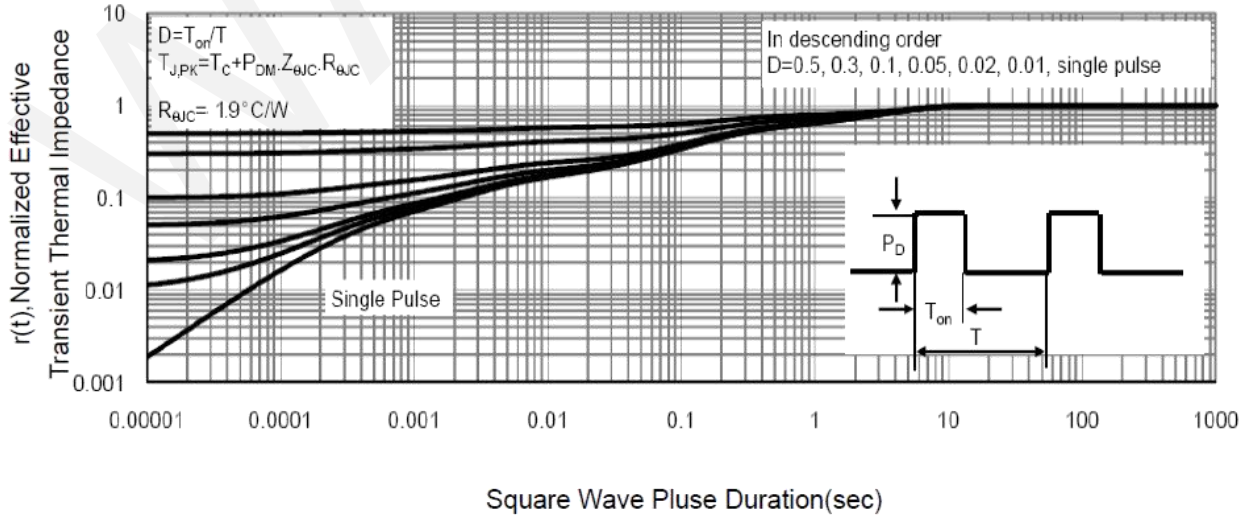
Safe Operation Area



Power De-rating

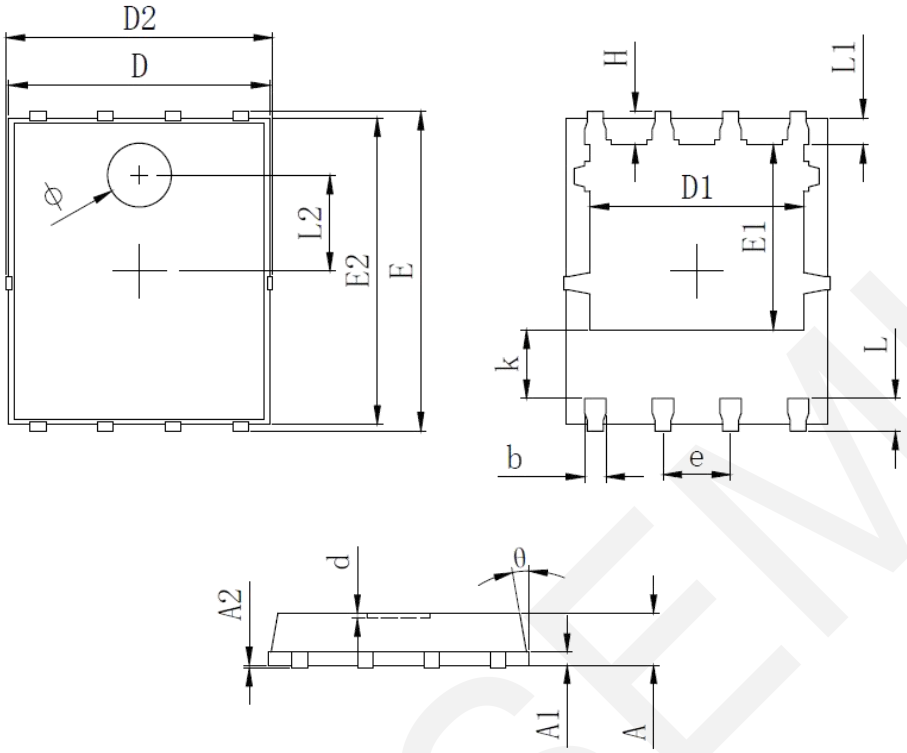


ID Current vs. Junction Temperature



Normalized Maximum Transient Thermal Impedance

7.Package Dimensions



SYMBOL	MILLIMETER		
	MIN	Typ.	MAX
A	0.900	1.000	1.100
A1	0.254 REF.		
A2	0~0.05		
D	4.824	4.900	4.976
D1	3.910	4.010	4.110
D2	4.924	5.000	5.076
E	5.924	6.000	6.076
E1	3.375	3.475	3.575
E2	5.674	5.750	5.826
b	0.350	0.400	0.450
e	1.270 TYP.		
L	0.534	0.610	0.686
L1	0.424	0.500	0.576
L2	1.800 REF.		
k	1.190	1.290	1.390
H	0.549	0.625	0.701
θ	8°	10°	12°
Φ	1.100	1.200	1.300
d			0.100

8. Important Notice

WAN SEMICONDUCTOR (NINGBO) CO.,LTD reserves the right to make corrections, enhancements, improvements and other changes to its semiconductor products and services and to discontinue any product or service. Buyers should obtain the latest relevant information before placing orders and should verify that such information is current and complete. All semiconductor products (also referred to herein as “components”) are sold subject to WANSEMI’s terms and conditions of sale supplied at the time of order acknowledgment.

WANSEMI warrants performance of its components to the specifications applicable at the time of sale, in accordance with the warranty in WANSEMI’s terms and conditions of sale of semiconductor products. Testing and other quality control techniques are used to the extent WANSEMI deems necessary to support this warranty. Except where mandated by applicable law, testing of all parameters of each component is not necessarily performed.

WANSEMI assumes no liability for applications assistance or the design of Buyers’ products. Buyers are responsible for their products and applications using WANSEMI components. To minimize the risks associated with Buyers’ products and applications, Buyers should provide adequate design and operating safeguards.

No WANSEMI components are authorized for use in FDA Class III (or similar life-critical medical equipment) unless authorized officers of the parties have executed a special agreement specifically governing such use.

Unless WANSEMI has specifically designated certain components which meet ISO/TS16949 requirements, mainly for automotive use, WANSEMI will not be responsible for any failure of such components to meet such requirements.