

# 30V, 3.9mΩ, 80A, Single N-Channel

#### 1.Features

- ♦ 30V MOSFET technology
- Low on-state resistance
- Fast switching
- Vgs±20V

## 2.Applications

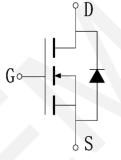
- Power Switching Application
- Load Switching



Pin Description

## 3.Absolute Max Ratings at Ta=25°C (Note1)

V <sub>DS</sub>	R <sub>DS(on)</sub> Typ.	I <sub>D</sub> Max.	
30V	3.9mΩ @ 10V	- 80A	
	6.5mΩ @ 4.5V		



Schematic Diagram

Parameter	Symbol	Maximum	Units		
Drain to Source Voltage	V <sub>DSS</sub>	30	V		
Gate to Source Voltage	V <sub>GSS</sub>	±20	V		
Drain Current (DC)	Ι <sub>D</sub>	80	А		
Drain Current (Pulse), PW≤300µs	I <sub>DP</sub>	280	А		
Total Dissipation	PD	46	W		
Avalanche Energy, Single Pulsed	E <sub>AS</sub>	150	mJ		
Junction Temperature	Tj	150	°C		
Storage Temperature	T <sub>stg</sub>	-55 to +150	°C		

Note 1: Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

#### 4. Thermal Resistance Ratings

Parameter	Symbol	Value	Unit
Junction to case	R <sub>θJC</sub>	2.72	°C/W

Note 2: When mounted on 1 inch square copper board  $t \le 10$  sec The value in any given application depends on the user's specific board design.



5. Electrical Characteristics at Ta=25°C (Note 3)						
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Units
Drain to Source Breakdown Voltage	V <sub>(BR)DSS</sub>	I <sub>D</sub> = 250μA, V <sub>GS</sub> = 0V	30			V
Zero-Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = 30V, V <sub>GS</sub> = 0V			1	μA
Gate to Source Leakage Current	I <sub>GSS1</sub>	$V_{GS}$ = ±20V, $V_{SS}$ = 0V			±100	nA
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>DS</sub> =250µA	1.0	1.75	2.5	V
Static Drain to Source On-State	R <sub>DS(on)</sub>	I <sub>D</sub> = 30A, V <sub>GS</sub> = 10V	-	3.9	4.5	mΩ
Resistance		I <sub>D</sub> = 20A, V <sub>GS</sub> = 4.5V	-	6.5	8	mΩ
Input Capacitance	Ciss	V <sub>GS</sub> =0V,		1615		pF
Output Capacitance	C <sub>oss</sub>	V <sub>DS</sub> =15V,		245		рF
Reverse Transfer Capacitance	Crss	Frequency=1.0MHz		216		pF
Turn-ON Delay Time	t <sub>d(on)</sub>			7.5		ns
Rise Time	tr	V <sub>DS</sub> = 15V, I <sub>D</sub> = 30A,		14.5		ns
Turn-OFF Delay Time	t <sub>d(off)</sub>	$V_{GS} = 10V,$ $R_G = 3\Omega$		35		ns
Fall Time	t <sub>f</sub>			9.6		ns
	Qg	V <sub>DS</sub> = 15V,		33.8		nC
Total Gate Charge	Q <sub>gs</sub>	$V_{GS} = 10V,$		8.5		nC
	Q <sub>gd</sub>	I <sub>D</sub> = 30A		7.5		nC
Diode Forward Voltage	V <sub>FSD</sub>	I <sub>S</sub> = 30A, V <sub>GS</sub> = 0		0.9	1.2	V

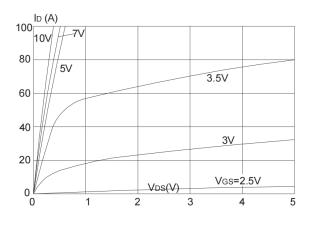
## 5.Electrical Characteristics at Ta=25°C (Note 3)

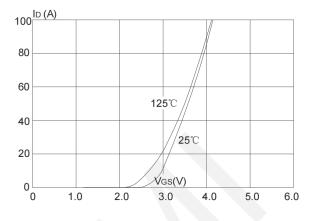
Note 3: Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.



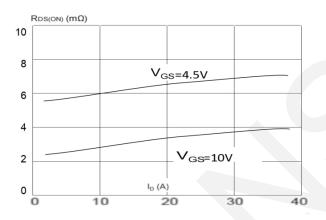


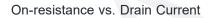
## 6. Typical electrical and thermal characteristics

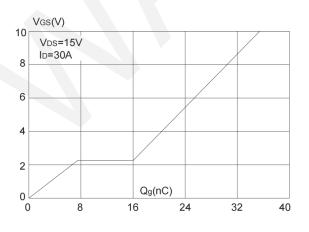




#### Output Characteristics

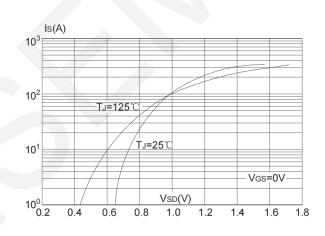




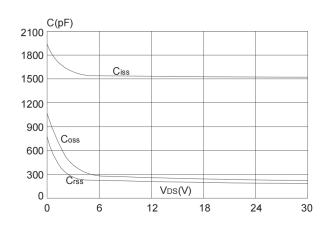


Gate Charge Characteristics

Typical Transfer Characteristics



**Body Diode Characteristics** 

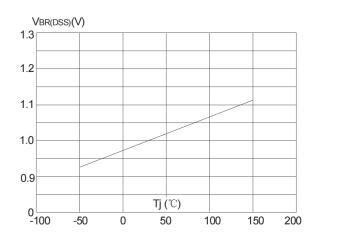


#### **Capacitance Characteristics**

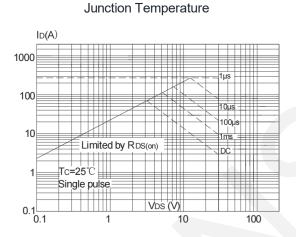
#### Rev.0.5

## WP3080K

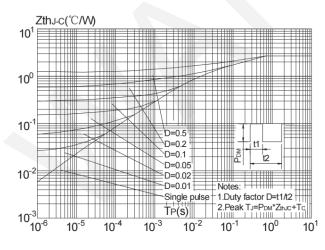




Normalized Breakdown Voltage vs.

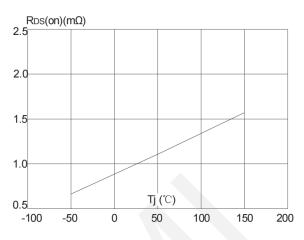




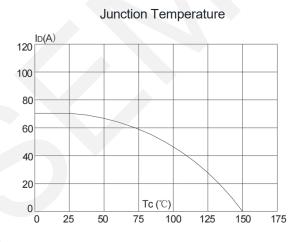


Maximum Effective Transient

Thermal Impedance, Junction-to-Case



Normalized on Resistance vs.



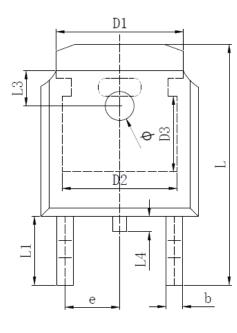
Maximum Continuous Drain Current vs.

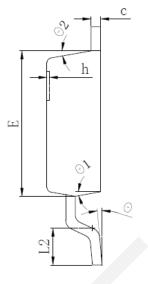
### Case Temperature

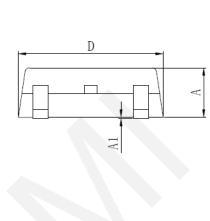
## Rev.0.5



## 7.Package Dimensions







SYMBOL	MILLIMETER			
51 MDUL	MIN	Тур.	MAX	
A	2.200	2.300	2. 400	
A1	0.000		0.127	
b	0.640	0.690	0.740	
c(电镀后)	0.460	0.520	0.580	
D	6.500	6.600	6.700	
D1	5.334 REF			
D2	4.826 REF			
D3	3.166 REF			
Е	6.000	6.100	6.200	
е	2.286 TYP			
h	0.000	0.100	0.200	
L	9.900	10.100	10.300	
L1	2.888 REF			
L2	1.400	1.550	1.700	
L3	1.600 REF			
L4	0.600	0.800	1.000	
φ	1.100	1.200	1.300	
θ	0° 8			
θ1	9° TYP			
θ2	θ 2 9° TYP			



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