

Enhancement Mode N-Channel Power MOSFET

 $SOT23-6/NMOS/14V/\pm10V/0.7V/2A/22.5m\Omega$

Rev_{0.5}





14V, 22.5mΩ, 2A, N-Channel MOSFET

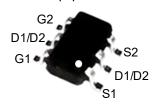
1.Features

- High Power and current handing capability
- Lead free product is acquired
- Surface Mount Package

V _{DS} Typ.	R _{DS(on)} Typ.	I _D Max.
14V	22.5mΩ @ 4.5V	2Α
	26.0mΩ @ 2.5V	ZA.

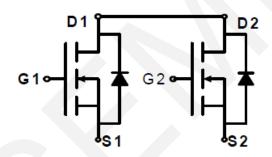
2.Applications

- Battery Protection
- Battery Powered Systems
- Power Management in Notebook Computer
- ◆ Portable Equipment



Pin Description

SOT23-6



Schematic Diagram

3. Package Marking and Ordering Information

Part no.	Marking	Package	PCS/Reel	PCS/CTN.
WP8205CDS6	8205A	SOT23-6	3,000	180,000

4.Absolute Max Ratings at Ta=25°C (Note1)

Parameter	Symbol	Maximum	Units
Drain to Source Voltage	V _{DSS}	14	V
Gate to Source Voltage	V _{GSS}	±10	V
Drain Current-Continuous	I _D	2	Α
Drain Current (Pulse)	I _{DM}	8	Α
Maximum Power Dissipation	P _D	1.9	W
Junction Temperature	Tj	150	°C
Storage Temperature	T _{stg}	-55 to +150	°C

Note 1: Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.



5.Electrical Characteristics at Ta=25°C (Note 2)

Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Units
Drain to Source Breakdown Voltage	$V_{(BR)DSS}$	$I_D = 250 \mu A, V_{GS} = 0 V$	14	17.2	-	V
Zero-Gate Voltage Drain Current	I _{DSS}	V _{DS} = 14V, V _{GS} = 0V	ı	i	1	μΑ
Gate-Body Leakage Current	I _{GSS}	$V_{GS} = \pm 10V$, $V_{DS} = 0V$	-	-	±100	nA
Gate Threshold Voltage	$V_{GS(th)}$	V_{DS} = V_{GS} , I_{DS} =250 μ A	0.4	0.7	1.0	V
Drain to Source On-State		$I_D = 2A, V_{GS} = 4.5V$	ı	22.5	30	mΩ
Resistance	R _{DS(on)}	I _D = 1A, V _{GS} = 2.5V	-	26	40	mΩ
Input Capacitance	C_{iss}	V _{GS} =0V,	-	370	-	pF
Output Capacitance	C_{oss}	V _{DS} =10V,	-	89	-	pF
Reverse Transfer Capacitance	C_{rss}	Frequency=1.0MHz	,	10	-	pF
Turn-ON Delay Time	t _{d(on)}		,	200	_	ns
Turn-ON Rise Time	t _r	$V_{DD} = 10V, I_D = 2A,$		236	-	ns
Turn-OFF Delay Time	$t_{\text{d(off)}}$	$V_{GS} = 4.5V, R_{GEN} = 10\Omega$	-	36	-	ns
Turn-ON Fall Time	t _f		-	165	-	ns
Total Gate Charge	Q_g	V _{DS} = 10V,	-	7.5	-	nC
Gate-Source Charge	Q_{gs}	$V_{GS} = 4.5V$,	-	3.0	-	nC
Gate-Drain Charge	Q_{gd}	I _D = 1A	-	1.5	-	nC
Diode Forward Voltage	V_{SD}	I _S = 2A, V _{GS} = 0V	0.4	0.8	1.2	V

Note 2: Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.



6. Typical electrical and thermal characteristics

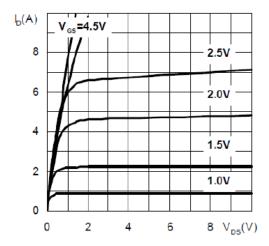


Figure 1 Output Characteristics

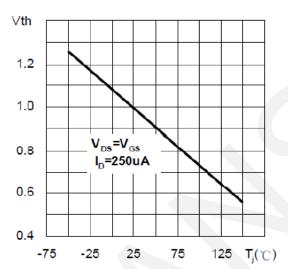


Figure 3 Thershold Voltage vs. Temperature

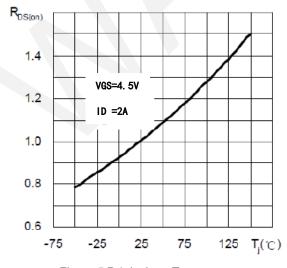


Figure 5 Rds(on) vs. Temperature

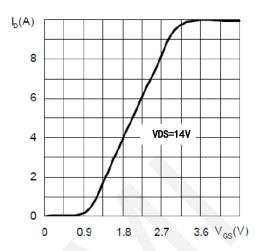


Figure 2 Transfer Characteristics

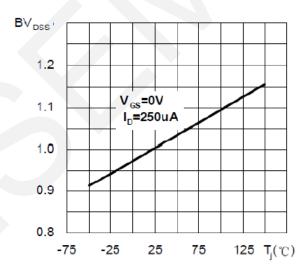


Figure 4 BVDSS vs. Temperature

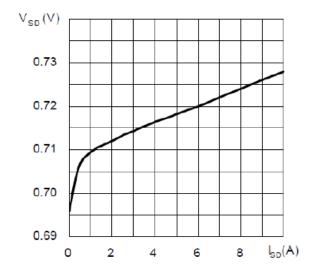
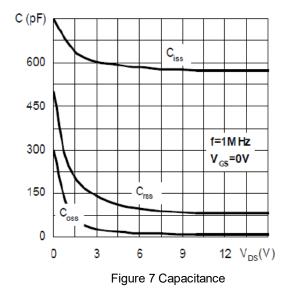
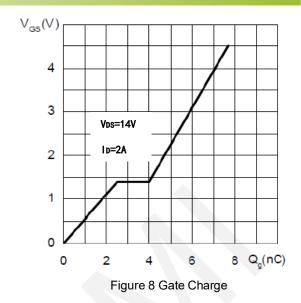
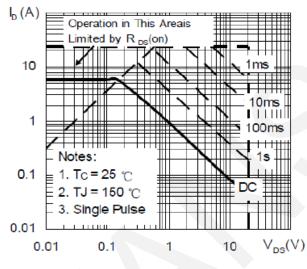


Figure 6 Source to Drain vs. Temperature









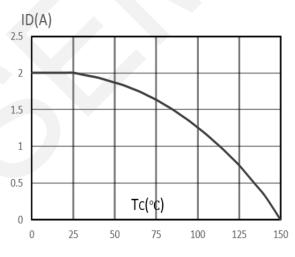


Figure 9 Safe Operating Area

Figure 10 Maximum Drain Current vs. Case Temperature

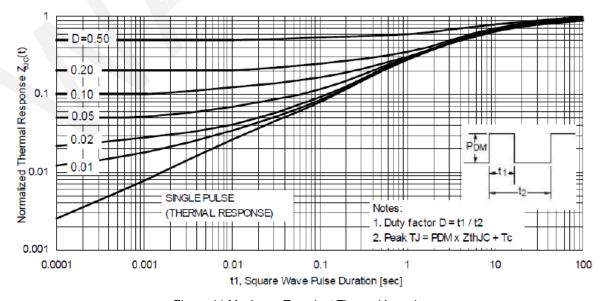
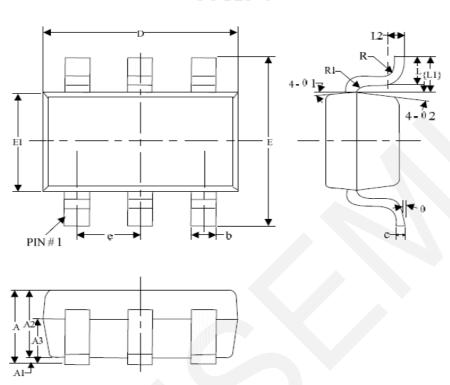


Figure 11 Maximum Transient Thermal Impedence WAN SEMICONDUCTOR (NINGBO) CO.,LTD



7.Package Dimensions

SOT23-6



Dimensions (unit: mm)

SYMBOL	MIN	NOM	MAX	SYMBOL	MIN	NOM	MAX
Α	-	_	1.30	е	0.85	0.95	1.05
A1	0	-	0.15	L	0.35	0.45	0.60
A2	0.90	1.10	1.30	L1	0.59REF		
A3	0.60	0.65	0.70	L2	0.25BSC		
b	0.39	-	0.49	R	0.05	-	-
С	0.12	-	0.19	R1	0.05	-	0.02
D	2.85	2.95	3.15	θ	0°	-	8°
E	2.60	2.80	3.00	θ1	3°	5°	7°
E1	1.55	1.65	1.75	θ2	6°	8°	10°



8.Important Notice

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