

Enhancement Mode Dual N-Channel Power MOSFET

PDFN3x3/NMOS/30V/ \pm 20V/1.8V/22A/10m Ω

Rev_{0.5}





30V, 10mΩ, 22A, Dual N-Channel

1.Features

- ◆ 30V MOSFET technology
- ◆ Low on-state resistance
- ◆ Fast switching
- ♦ Vgs±20V

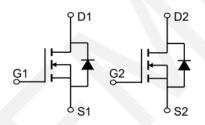
V D3	TOS(OII) TYP.	ID WIGH.
201/	10mΩ @ 10V	22A
30V	14.5mΩ @ 4.5V	22A

2.Applications

- ♦ Power Switching Application
- Load Switching



Pin Description



Schematic Diagram

3. Package Marking and Ordering Information

Part no.	Marking	Package	PCS/Reel	PCS/CTN.	
WP3040DP3	WP3040DP3	PDFN3x3	5,000	50,000	

4. Absolute Max Ratings at Ta=25°C (Note1)

Parameter	Symbol	Maximum	Units
Drain to Source Voltage	V _{DSS}	30	V
Gate to Source Voltage	V _{GSS}	±20	V
Drain Current (DC)	ID	22	А
Drain Current (Pulse), PW≤300μs	I _{DP}	88	А
Total Dissipation	P _D	34	W
Avalanche Energy, Single Pulsed	Eas	30	mJ
Junction Temperature	Tj	150	°C
Storage Temperature	T _{stg}	-55 to +150	°C

Note 1: Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.



5. Thermal Resistance Ratings (Note 2)

Parameter	Symbol	Value	Unit
Junction to case	Rejc	3.7	°C/W
Junction to Ambient	Reja	59	°C/W

Note 2: When mounted on 1 inch square copper board t ≤ 10sec The value in any given application depends on the user's specific board design.

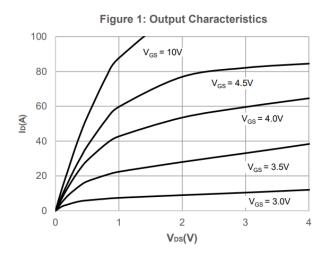
6.Electrical Characteristics at Ta=25°C (Note 3)

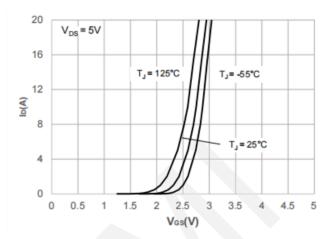
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Units
Drain to Source Breakdown Voltage	V _{(BR)DSS}	$I_D = 250 \mu A$, $V_{GS} = 0 V$	30			V
Zero-Gate Voltage Drain Current	I _{DSS}	V _{DS} = 30V, V _{GS} = 0V			1.0	μA
Gate to Source Leakage Current	Igss	$V_{GS} = \pm 20V$, $V_{DS} = 0V$			±100	nA
Gate Threshold Voltage	$V_{\text{GS(th)}}$	V _{DS} =V _{GS} , I _{DS} =250µA	1.0	1.8	2.5	V
Static Drain to Source On-State		I _D = 20A, V _{GS} = 10V		10	13	mΩ
Resistance	R _{DS(on)}	$I_D = 20A$, $V_{GS} = 4.5V$		14.5	17	mΩ
Input Capacitance	Ciss	V _{GS} =0V,		1002		pF
Output Capacitance	Coss	V _{DS} =15V,		131		pF
Reverse Transfer Capacitance	Crss	Frequency=1.0MHz		105		pF
Turn-ON Delay Time	t _{d(on)}			6		ns
Rise Time	t _r	V _{DD} = 15V, I _{DS} = 30A,		9		ns
Turn-OFF Delay Time	$t_{d(off)}$	$V_{GS} = 10V$, $R_G = 3\Omega$		22		ns
Fall Time	t _f			5		ns
	Qg	V _{DS} = 15V, V _{GS} = 0 to 10V,		20		nC
Total Gate Charge	Qgs			4		nC
$Q_{ m gd}$		I _{DS} = 30A		5		nC
Diode Forward Voltage	V _{FSD}	I _S = 20A, V _{GS} = 0V		0.85	1.2	V

Note 3: Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

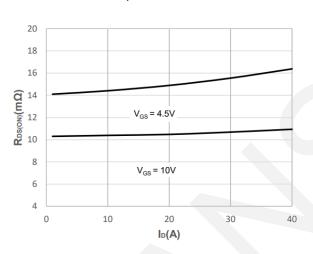


7. Typical electrical and thermal characteristics

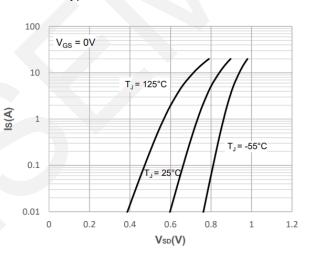




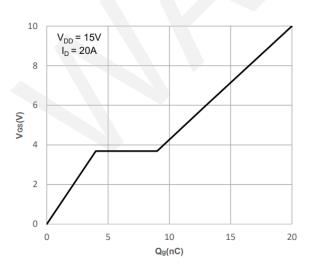
Output Characteristics



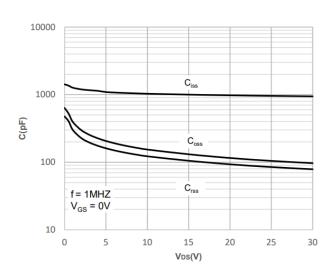
Typical Transfer Characteristics



On-resistance vs. Drain Current



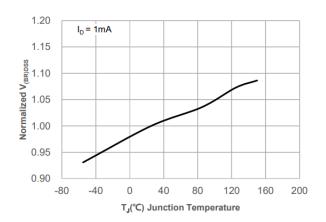
Body Diode Characteristics

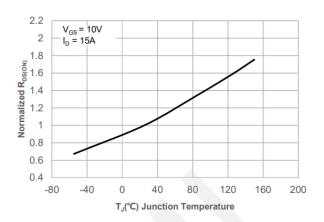


Gate Charge Characteristics

Capacitance Characteristics

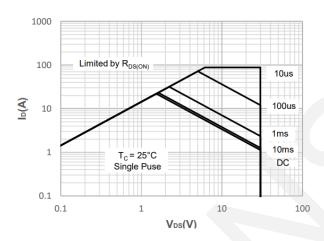






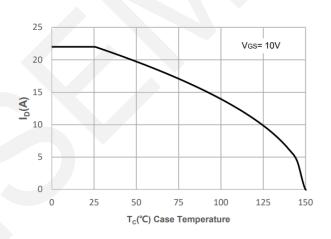
Normalized Breakdown Voltage vs.

Junction Temperature

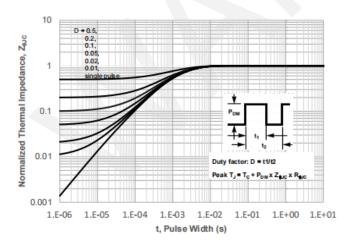


Normalized on Resistance vs.

Junction Temperature

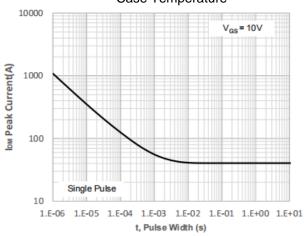


Maximum Safe Operating Area



Maximum Continuous Drain Current vs.

Case Temperature

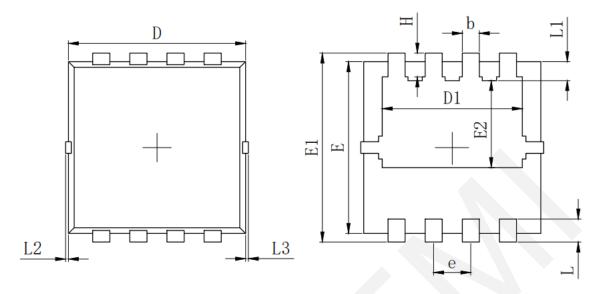


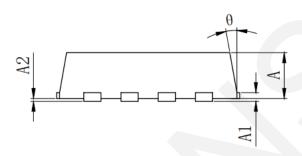
Normalized Maximum Transient Thermal Impedance

Peak Current Capacity



8.Package Dimensions





SYMBOL	1	MILLIMETER	
SYMBOL	MIN	Тур.	MAX
A	0. 700	0.800	0.900
A1		0. 152 REF.	
A2		0~0.05	
D	3. 000	3. 100	3. 200
D1	2. 300	2. 450	2.600
Е	2. 900	3.000	3. 100
E1	3. 150	3. 300	3. 450
E2	1. 320	1.520	1. 720
b	0. 200	0.300	0.400
е	0. 550	0.650	0. 750
L	0. 300	0.400	0.500
L1	0. 180	0. 330	0.480
L2	0~0.100		
L3	0~0. 100		
Н	0. 315	0. 415	0. 515
θ	8°	10°	12°



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