



WANSEMI
万芯半导体

WP3026DP3

Enhancement Mode N+P-Channel Power MOSFET

PDFN3x3/N+P/30V.-30V/ $\pm$ 20V/1.5V.-1.5V/60A.-50A/

6.5m Ω .13.5m Ω

Rev0.1

30V N+P-Channel MOSFET

1.Features

- ◆ High power and current handing capability
- ◆ Lead free product is acquired
- ◆ Fast switching
- ◆ Surface mount package
- ◆ 100% RG Tested
- ◆ 100% UIS Tested

2.Applications

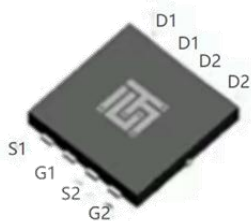
- ◆ DC motor
- ◆ PWM applications

◆ N-Channel

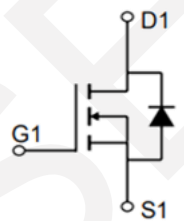
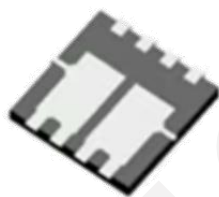
V_{DS}	$R_{DS(on)}$ Typ.	I_D
30V	6.5mΩ @ 10V	60A
	10mΩ @ 4.5V	

◆ P-Channel

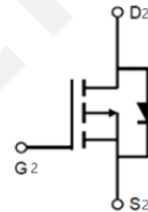
V_{DS}	$R_{DS(on)}$ Typ.	I_D
-30V	13.5mΩ @ -10V	-50A
	18mΩ @ -4.5V	



PDFN3x3
Pin Description



N-Channel



P-Channel

Schematic Diagram

3.Package Marking and Ordering Information

Part no.	Marking	Package	PCS/Tube	PCS/CTN.
WP3026DP3	3026D	PDFN3x3	5,000	50,000

4.Absolute Max Ratings at Ta=25°C (Note1)

Parameter	Symbol	N-channel	P-channel	Units
Drain to Source Voltage	V_{DSS}	30	-30	V
Gate to Source Voltage	V_{GSS}	±20	±20	V
Drain Current (DC)	I_D	60	-50	A
Drain Current (Pulse), PW≤300μs	I_{DM}	240	-200	A
Total Dissipation	P_D	28	25	W
Junction Temperature	T_j	-55 to +150		°C
Storage Temperature	T_{stg}			

Note 1: Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

5. Thermal Resistance Ratings (Note 2)

Parameter	Symbol	N-channel	P-channel	Unit
Maximum Junction-to-Ambient	$R_{\theta JA}$	59	62	$^{\circ}\text{C/W}$

Note 2: When mounted on 1 inch square copper board $t \leq 10\text{sec}$ The value in any given application depends on the user's specific board design.

6. Electrical Characteristics at $T_a=25^{\circ}\text{C}$ (Note 3)
N-Channel

Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Units
Drain to Source Breakdown Voltage	$V_{(BR)DSS}$	$I_D = 250\mu\text{A}$, $V_{GS} = 0\text{V}$	30	-	-	V
Zero-Gate Voltage Drain Current	I_{DSS}	$V_{DS} = 30\text{V}$, $V_{GS} = 0\text{V}$	-	-	1	μA
Gate to Source Leakage Current	I_{GSS}	$V_{GS} = \pm 20\text{V}$, $V_{DS} = 0\text{V}$	-	-	± 100	nA
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}$, $I_{DS}=250\mu\text{A}$	1.2	1.5	2.2	V
Static Drain to Source On-State Resistance	$R_{DS(on)}$	$I_D = 20\text{A}$, $V_{GS} = 10\text{V}$	-	6.5	8.0	m Ω
		$I_D = 10\text{A}$, $V_{GS} = 4.5\text{V}$	-	10	15	m Ω
Input Capacitance	C_{iss}	$V_{GS}=0\text{V}$, $V_{DS}=15\text{V}$, Frequency=1.0MHz	-	1174	-	pF
Output Capacitance	C_{oss}		-	162	-	pF
Reverse Transfer Capacitance	C_{rss}		-	130	-	pF
Turn-ON Delay Time	$t_{d(on)}$	$V_{DD} = 15\text{V}$ $V_{GS} = 10\text{V}$ $R_G = 3\Omega$ $I_D = 15\text{A}$	-	6.7	-	ns
Rise Time	t_r		-	15	-	ns
Turn-OFF Delay Time	$t_{d(off)}$		-	25	-	ns
Fall Time	t_f		-	5.6	-	ns
Total Gate Charge	Q_g	$V_{DS} = 15\text{V}$, $V_{GS} = 0 \text{ to } 10\text{V}$, $I_D = 20\text{A}$	-	23	-	nC
	Q_{gs}		-	4.6	-	nC
	Q_{gd}		-	5.5	-	nC
Diode Forward Voltage	V_{FSD}	$I_S = 20\text{A}$, $V_{GS} = 0\text{V}$	0.5	-	1.2	V

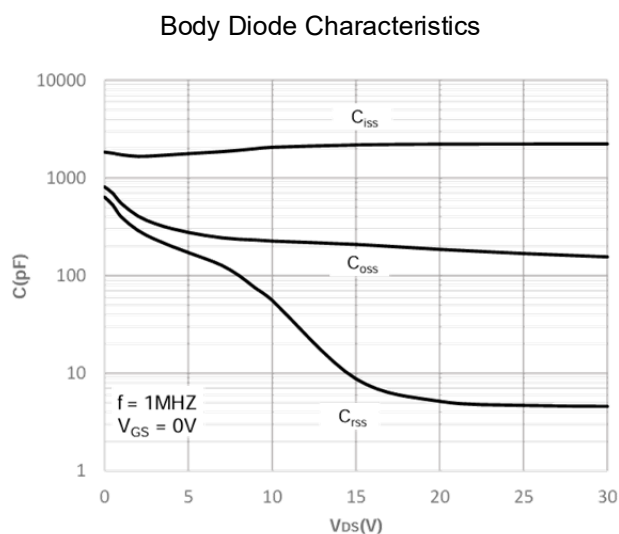
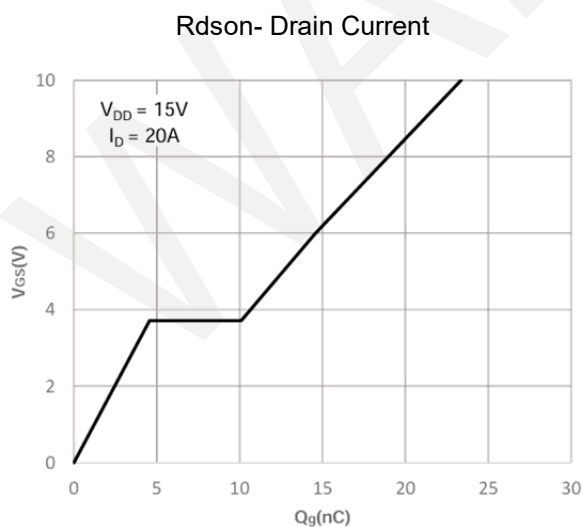
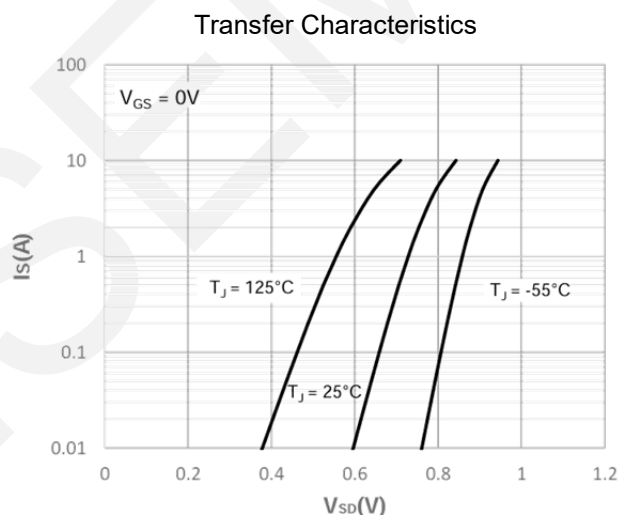
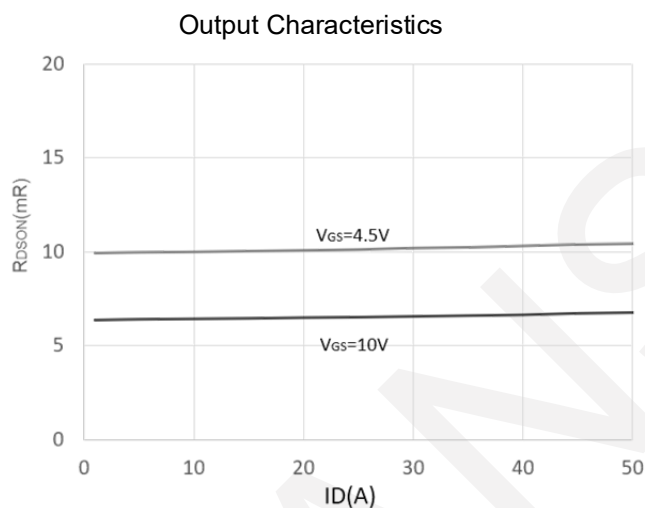
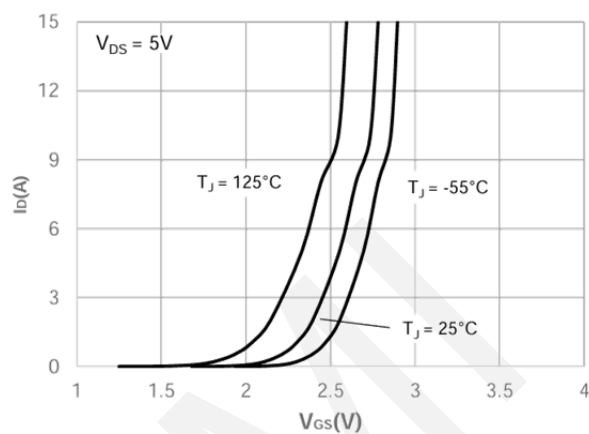
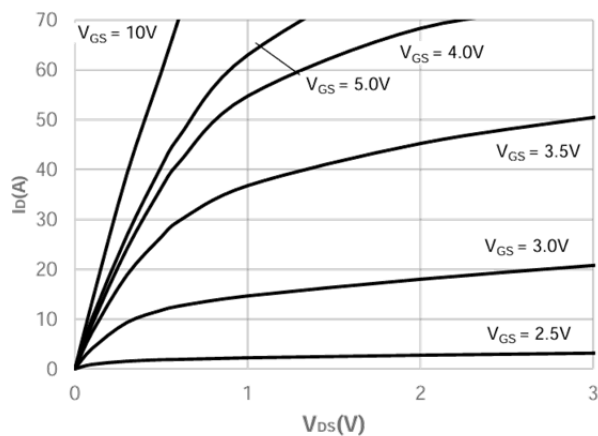
P-Channel

Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Units
Drain to Source Breakdown Voltage	$V_{(BR)DSS}$	$I_D = -250\mu A, V_{GS} = 0V$	-30	-	-	V
Zero-Gate Voltage Drain Current	I_{DSS}	$V_{DS} = -30V, V_{GS} = 0V$	-	-	-1	μA
Gate to Source Leakage Current	I_{GSS}	$V_{GS} = \pm 20V, V_{DS} = 0V$	-	-	± 100	nA
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_{DS}=-250\mu A$	-1.0	-1.5	-2.5	V
Static Drain to Source On-State Resistance	$R_{DS(on)}$	$I_D = -20A, V_{GS} = -10V$	-	13.5	17	m Ω
		$I_D = -10A, V_{GS} = -4.5V$	-	18	25	m Ω
Input Capacitance	C_{iss}	$V_{GS}=0V,$ $V_{DS}=-15V,$ Frequency=1.0MHz	-	1430	-	pF
Output Capacitance	C_{oss}		-	139	-	pF
Reverse Transfer Capacitance	C_{rss}		-	118	-	pF
Turn-ON Delay Time	$t_{d(on)}$	$V_{DD} = -15V$ $V_{GS} = -10V$ $R_{GEN}=3\Omega,$ $I_D=-15A,$	-	7	-	ns
Rise Time	t_r		-	1.7	-	ns
Turn-OFF Delay Time	$t_{d(off)}$		-	38	-	ns
Fall Time	t_f		-	50	-	ns
Total Gate Charge	Q_g	$V_{DS} = -15V,$ $V_{GS} = 0 \text{ to } -10V,$ $I_D = -15A$	-	25	-	nC
	Q_{gs}		-	5.1	-	nC
	Q_{gd}		-	4.5	-	nC
Diode Forward Voltage	V_{FSD}	$I_S = -20A, V_{GS} = 0V$	-0.5	-	-1.2	V

Note 3: Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

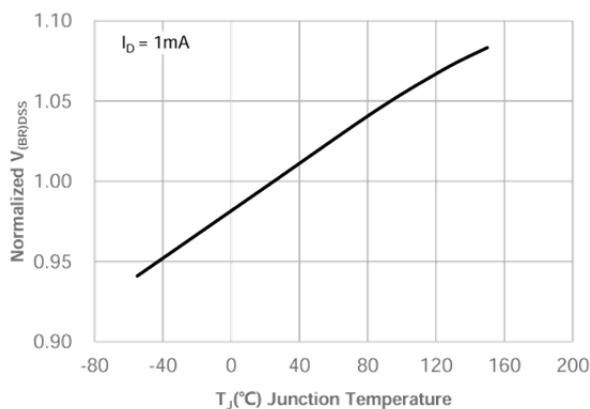
7. Typical electrical and thermal characteristics

N-Channel

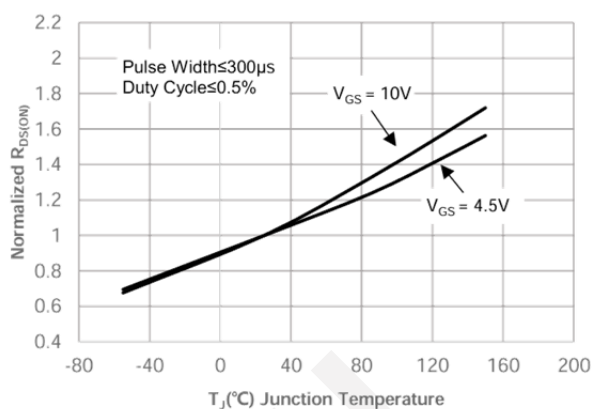


Gate Charge

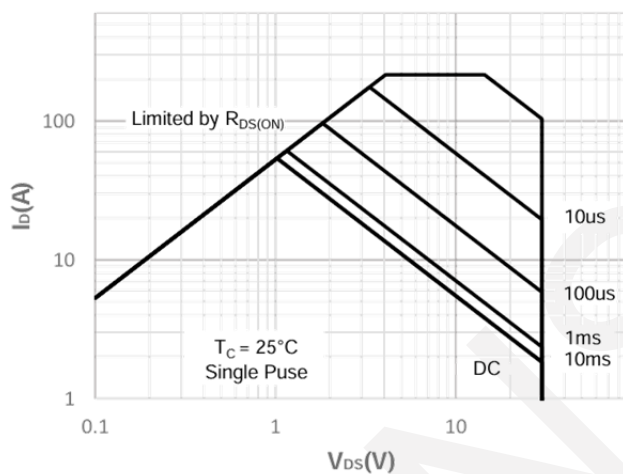
Capacitance Characteristics



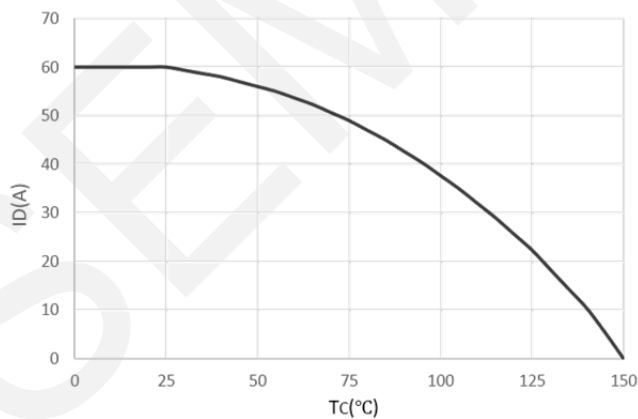
Normalized Breakdown voltage vs. Junction Temperature



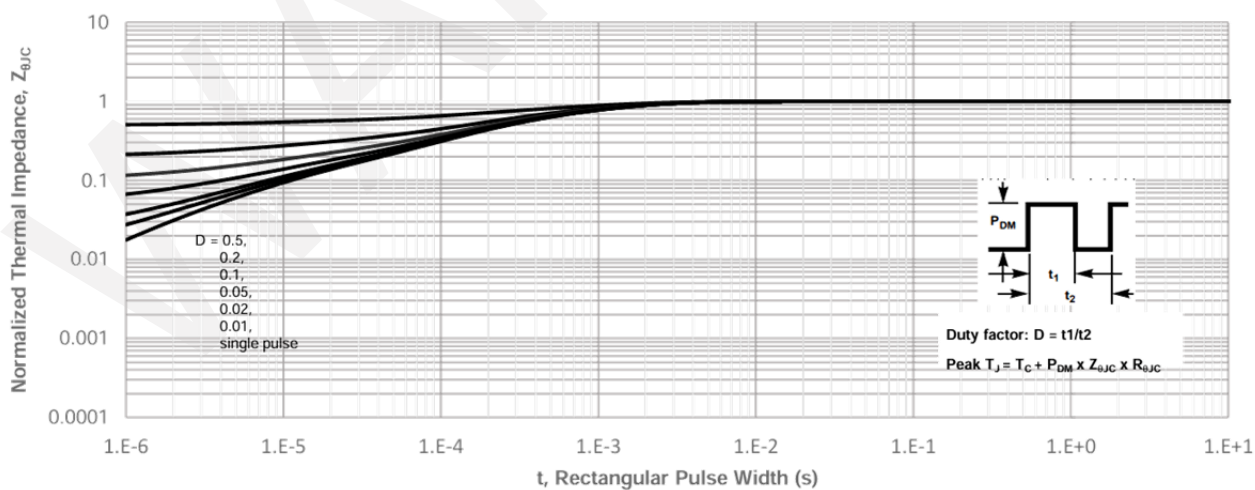
Normalized on Resistance vs. Junction Temperature



Safe Operation Area

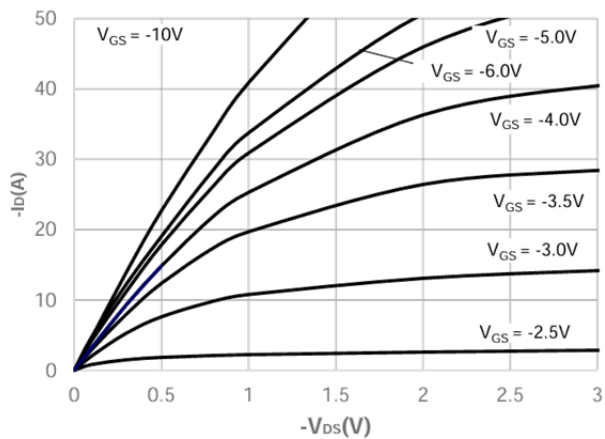


Current De-rating

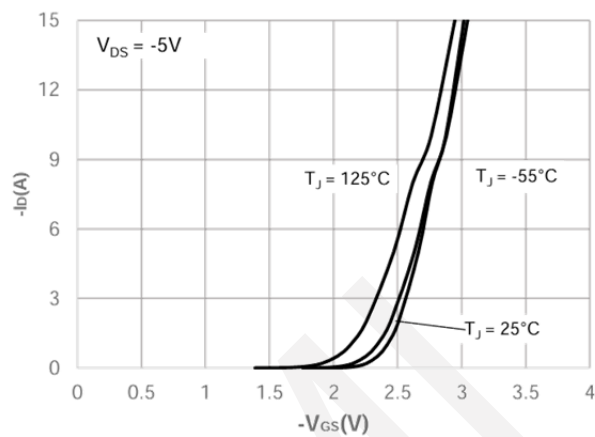


Normalized Maximum Transient Thermal Impedance

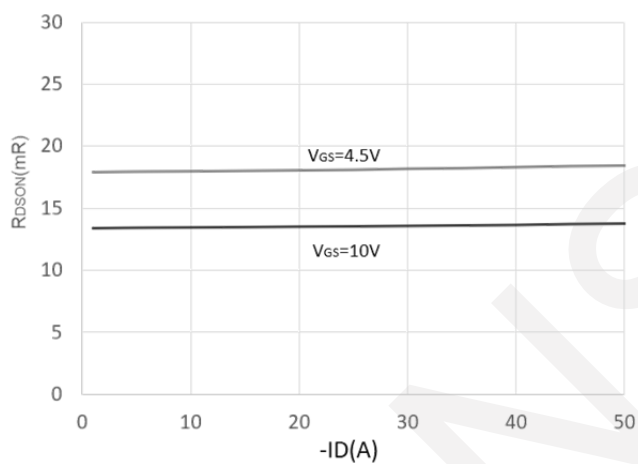
P-Channel



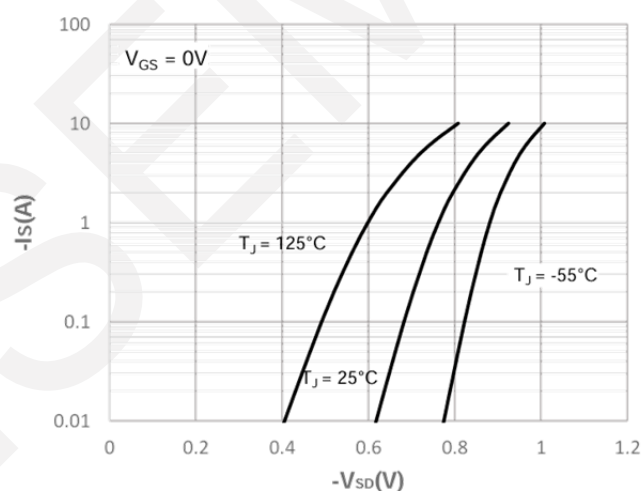
Output Characteristics



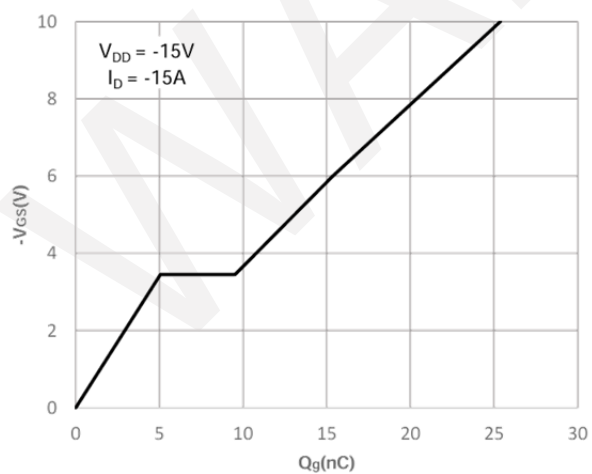
Transfer Characteristics



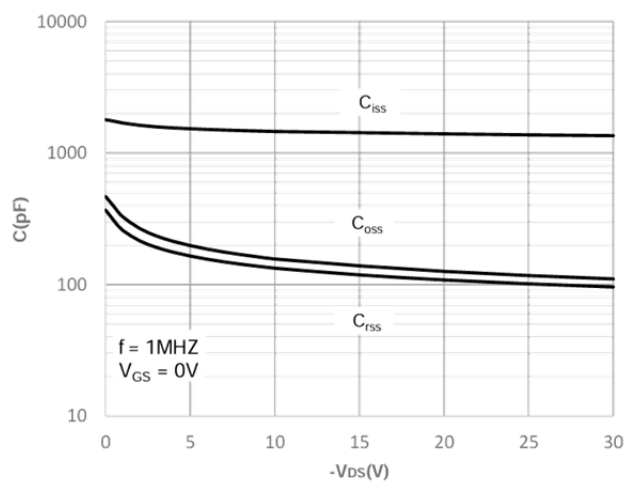
Rdson- Drain Current



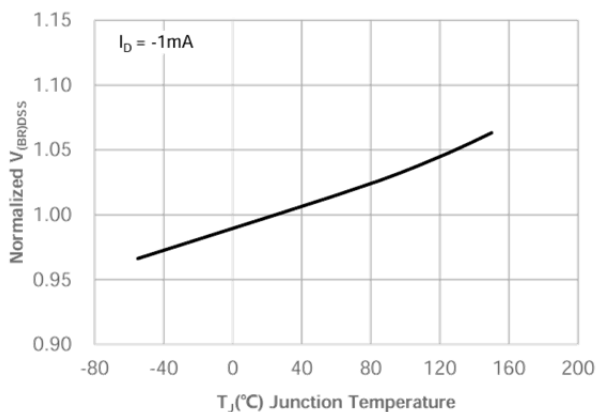
Body Diode Characteristics



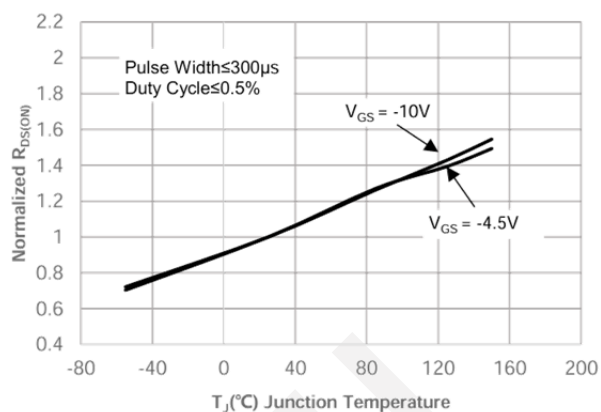
Gate Charge



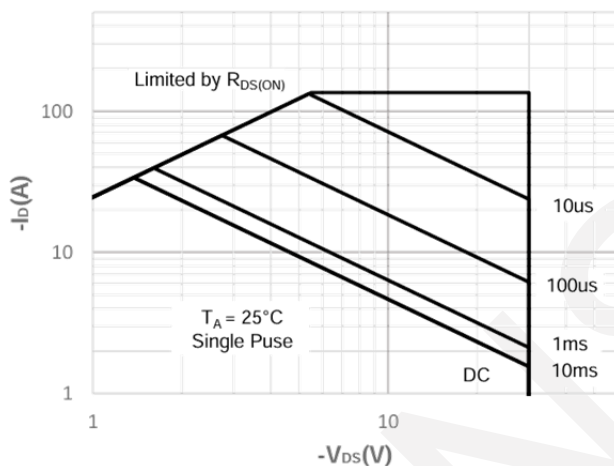
Capacitance Characteristics



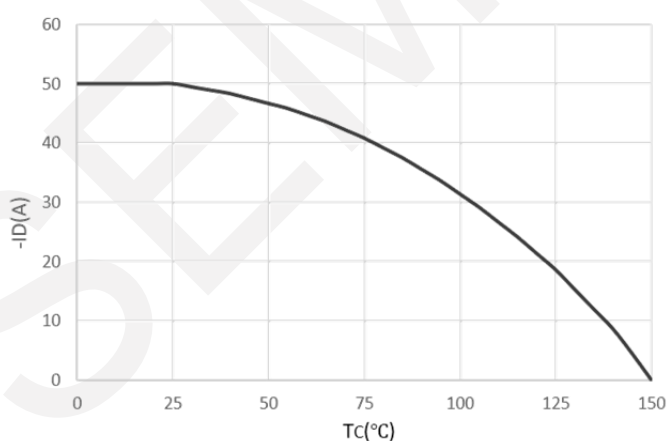
Normalized Breakdown voltage vs. Junction Temperature



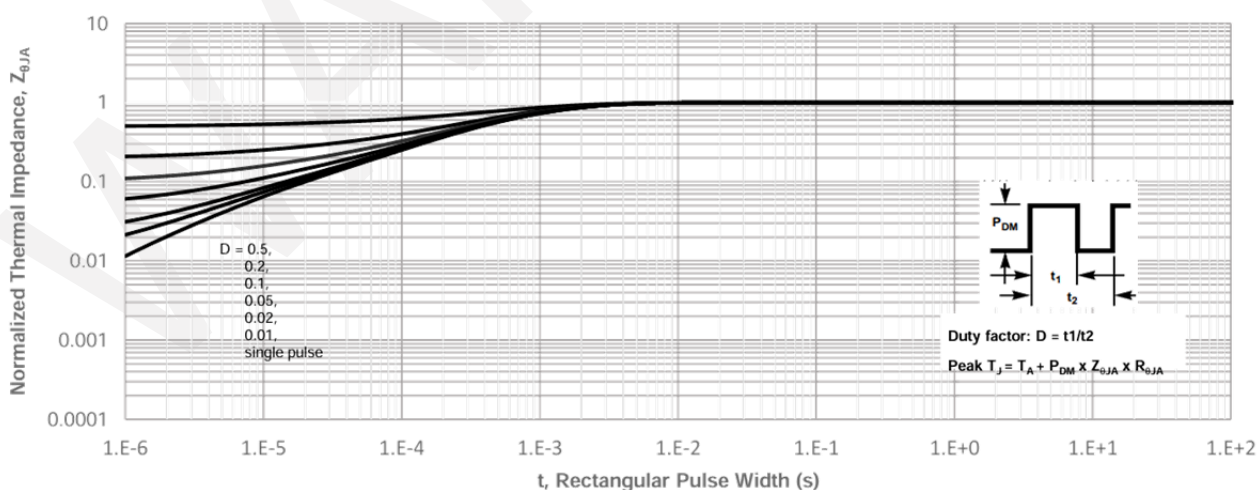
Normalized on Resistance vs. Junction Temperature



Safe Operation Area

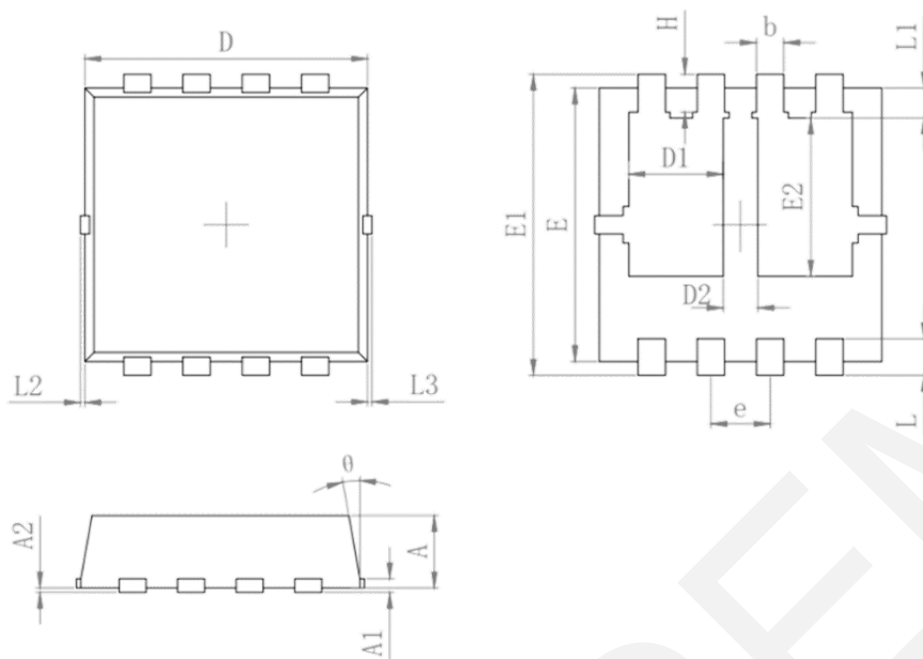


Current De-rating



Normalized Maximum Transient Thermal Impedance

8.Package Dimensions



SYMBOL	MILLIMETER	
	MIN	MAX
A	0.700	0.900
A1	0.152 REF.	
A2	0~0.05	
D	3.000	3.200
D1	0.935	1.135
D2	0.280	0.480
E	2.900	3.100
E1	3.150	3.450
E2	1.535	1.935
b	0.200	0.400
e	0.550	0.750
L	0.300	0.500
L1	0.180	0.480
L2	0~0.100	
L3	0~0.100	
H	0.315	0.515
θ	8°	12°

9. Important Notice

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