

# **Enhancement Mode N-Channel Power MOSFET**

PDFN5X6/NMOS/30V/ $\pm$ 20V/1.4V/130A/2.0m $\Omega$ 

Rev1.1





## 30V, 2.0mΩ, 130A, N-Channel MOSFET

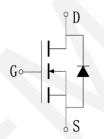
#### 1.Features

- Advanced Trench Technology
- ◆ Excellent R<sub>DS(ON)</sub> and Low Gate Charge
- ◆ Lead Free

- ◆ Load Switch
- PWM Application
- Power Management



V <sub>DS</sub>	R <sub>DS(on)</sub> Typ.	I <sub>D</sub> Max.	
30V	2.0mΩ @ 10V	4004	
	2.7mΩ @ 4.5V	130A	



Schematic Diagram

### 3. Package Marking and Ordering Information

Part no.	Marking	Package	PCS/Reel	PCS/CTN.
WP30H50KPA	WP30H50KPA	PDFN5X6	5,000	50,000

#### 4.Absolute Max Ratings at Ta=25°C (Note1)

Parameter	Symbol	Maximum	Units
Drain to Source Voltage	V <sub>DSS</sub>	30	V
Gate to Source Voltage	V <sub>GSS</sub>	±20	V
Drain Current (DC)	l <sub>D</sub>	130	А
Drain Current (Pulse), PW≤300μs	I <sub>DP</sub>	520	А
Total Dissipation	P <sub>D</sub>	347	W
Avalanche Energy, Single Pulsed	Eas	361	mJ
Junction Temperature	Tj	150	°C
Storage Temperature	T <sub>stg</sub>	-55 to +150	°C

Note 1: Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.



#### 5. Thermal Resistance Ratings (Note2)

Parameter	Symbol	Value	Unit
Junction to Ambient	Reja	30	°C/W
Junction to case	Rejc	0.36	°C/W

Note 2: When mounted on 1 inch square copper board  $t \le 10$ sec The value in any given application depends on the user's specific board design.

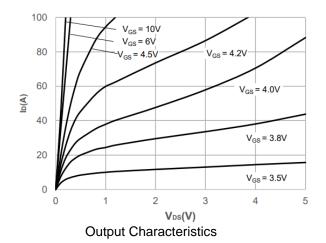
## 6.Electrical Characteristics at Ta=25°C (Note 3)

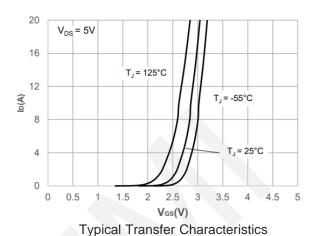
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Units
Drain to Source Breakdown Voltage	V <sub>(BR)DSS</sub>	$I_D = 250 \mu A$ , $V_{GS} = 0 V$	30		-	V
Zero-Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = 30V, V <sub>GS</sub> = 0V	ı	1	1	μA
Gate to Source Leakage Current	I <sub>GSS1</sub>	$V_{GS} = \pm 20V, V_{SS} = 0V$	,	-	±100	nA
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>DS</sub> =250µA	1.0	1.4	2.5	V
Static Drain to Source On-State	D	I <sub>D</sub> =20A, V <sub>GS</sub> = 10V	1	2.0	2.8	mΩ
Resistance	R <sub>DS(on)</sub>	I <sub>D</sub> = 10A, V <sub>GS</sub> = 4.5V	-	2.7	4.4	mΩ
Input Capacitance	Ciss	V <sub>GS</sub> =0V,	-	5441	-	pF
Output Capacitance	Coss	V <sub>DS</sub> =15V,	-	494	-	pF
Reverse Transfer Capacitance	Crss	Frequency=1.0MHz	-	366	-	pF
Turn-on Delay Time	t <sub>d(on)</sub>		-	10	-	ns
Rise Time	t <sub>r</sub>	V <sub>DD</sub> = 15V, I <sub>D</sub> =30A	-	19	-	ns
Turn-off Delay Time	$t_{d(off)}$	$V_{GS} = 10V$ , $R_{GEN} = 3\Omega$ ,	-	50	-	ns
Fall Time	t <sub>f</sub>		-	20	-	ns
	Qg	$V_{DD}$ = 30V, $V_{GS}$ = 0 to 10V, $I_{D}$ = 15A	-	67	-	nC
Total Gate Charge	Qgs		-	11	-	nC
	Q <sub>gd</sub>		-	19	-	nC
Diode Forward Voltage	$V_{FSD}$	I <sub>S</sub> = 20A, V <sub>GS</sub> = 0	-	-	1.2	V

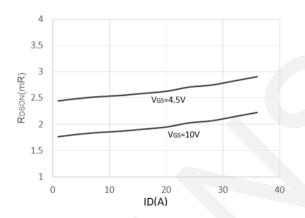
Note 3: Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

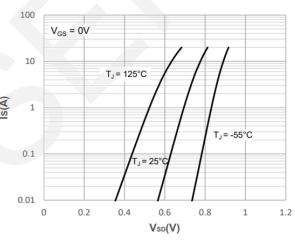


#### 7. Typical electrical and thermal characteristics



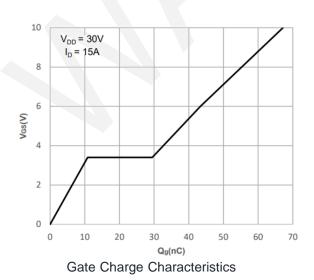


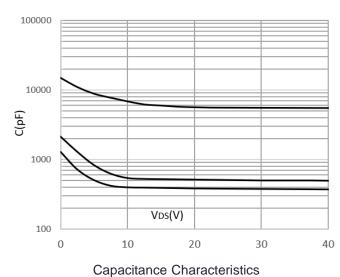




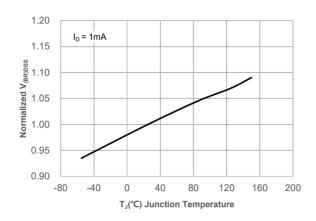
On-resistance vs. Drain Current

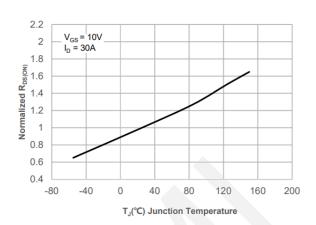
**Body Diode Characteristics** 





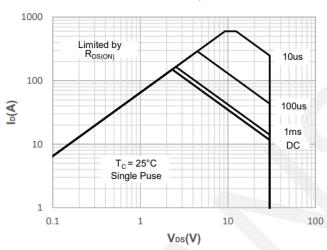






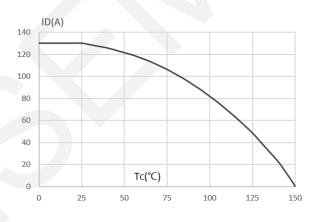
Normalized Breakdown Voltage vs.

Junction Temperature



Normalized on Resistance vs.

Junction Temperature



Maximum Safe Operating Area

10
0=0.5.
0.20.1.
0.05.
0.02.
0.01.
sindle pulse

0.01

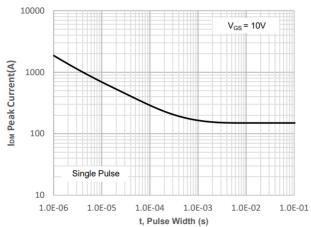
Duty factor: D = t1/12
Peak T, = Tc + Polk x Zeic x Reic

0.001

1.0E-06
1.0E-05
1.0E-04
1.0E-03
1.0E-02
1.0E-01
t, Pulse Width (s)

Maximum Continuous Drain Current vs.

Case Temperature

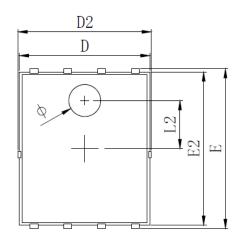


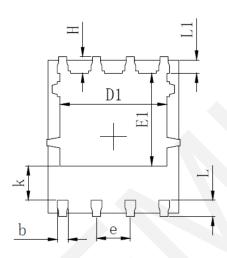
Normalized Maximum Transient Thermal Impedance

Peak Current Capacity



# 8.Package Dimensions





SYMBOL	MILLIMETER			
	MIN	Тур.	MAX	
A	0. 900	1. 000	1. 100	
A1		0.254 REF.		
A2		0~0. 05		
D	4. 824	4. 900	4. 976	
D1	3. 910	4. 010	4. 110	
D2	4. 924	5. 000	5. 076	
Е	5. 924	6. 000	6. 076	
E1	3. 375	3. 475	3. 575	
E2	5. 674	5. 750	5. 826	
b	0. 350	0. 400	0. 450	
e	1.270 TYP.			
L	0. 534	0. 610	0.686	
L1	0. 424	0. 500	0. 576	
L2	1.800 REF.			
k	1. 190	1. 290	1. 390	
Н	0. 549	0. 625	0. 701	
θ	8°	10°	12°	
ф	1. 100	1. 200	1. 300	
d			0. 100	



#### 9. Important Notice

WAN SEMICONDUCTOR (NINGBO) CO.,LTD reserves the right to make corrections, enhancements, improvements and other changes to its semiconductor products and services and to discontinue any product or service. Buyers should obtain the latest relevant information before placing orders and should verify that such information is current and complete. All semiconductor products (also referred to herein as "components") are sold subject to WANSEMI's terms and conditions of sale supplied at the time of order acknowledgment.

WANSEMI warrants performance of its components to the specifications applicable at the time of sale, in accordance with the warranty in WANSEMI's terms and conditions of sale of semiconductor products. Testing and other quality control techniques are used to the extent WANSEMI deems necessary to support this warranty. Except where mandated by applicable law, testing of all parameters of each component is not necessarily performed.

WANSEMI assumes no liability for applications assistance or the design of Buyers' products. Buyers are responsible for their products and applications using WANSEMI components. To minimize the risks associated with Buyers' products and applications, Buyers should provide adequate design and operating safeguards.

No WANSEMI components are authorized for use in FDA Class III (or similar life-critical medical equipment) unless authorized officers of the parties have executed a special agreement specifically governing such use.

Unless WANSEMI has specifically designated certain components which meet ISO/TS16949 requirements, mainly for automotive use, WANSEMI will not be responsible for any failure of such components to meet such requirements.