WANSEMI
万代半导体

WP9926B

Enhancement Mode N-Channel Power MOSFET

SOP8/NMOS/20V/±12V/0.75V/6A/18mΩ

Rev0.2

20V, 18mΩ, 6A, N-Channel Enhancement Mode Power MOSFET

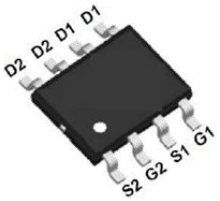
1.Features

- High Power and current handing capability
- Lead free product is acquired
- Surface Mount Package

V_{DS} Typ	$R_{DS(on)}$ Typ.	I_D Max.
20V	18mΩ @ 4.5V	6A
	25mΩ @ 2.5V	

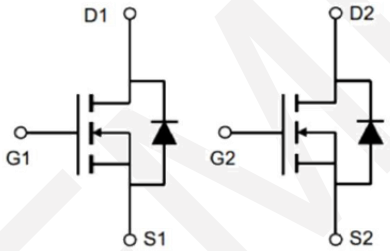
2.Applications

- Battery protection
- Load Switch
- Power management



SOP8

Pin Description



Schematic Diagram

3.Package Marking and Ordering Information

Part no.	Marking	Package	PCS/Tube	PCS/CTN.
WP9926B	9926B	SOP8	3,000	60,000

4.Absolute Max Ratings at Ta=25℃ (Note1)

Parameter	Symbol	Maximum	Units
Drain to Source Voltage	V_{DSS}	20	V
Gate to Source Voltage	V_{GSS}	±12	V
Drain Current-Continuous	I_D	6	A
Drain Current (Pulse)	I_{DM}	20	A
Maximum Power Dissipation	P_D	2	W
Operating Junction and Storage Temperature Range	T_j, T_{stg}	-55 to +150	℃

Note 1: Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

5.Thermal Characteristic

Parameter	Symbol	Value	Unit
Thermal Resistance, Junction to Ambient (Note 2)	$R_{\theta JA}$	56	℃/W

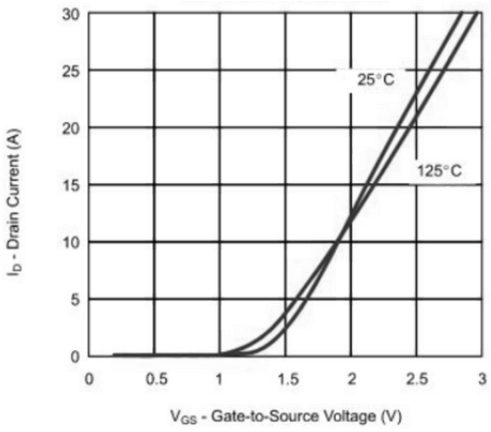
Note 2: When mounted on 1 inch square copper board t ≤ 10sec The value in any given application depends on the user's specific board design.

6.Electrical Characteristics at Ta=25℃ (Note 3)

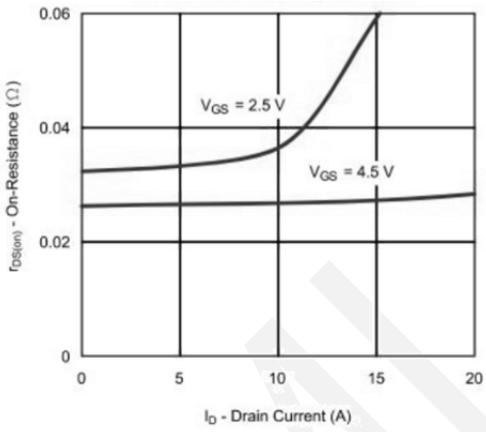
Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Units
Drain to Source Breakdown Voltage	$V_{(BR)DSS}$	I_D = 250μA, V_{GS} = 0V	20			V
Zero-Gate Voltage Drain Current	I_{DSS}	V_{DS} = 20V, V_{GS} = 0V			1	μA
Gate-Body Leakage Current	I_{GSS}	V_{GS} = ±12V, V_{DS} = 0V			±10	μA
Gate Threshold Voltage	$V_{GS(th)}$	V_{DS}=V_{GS}, I_{DS}=250μA	0.45	0.75	1.2	V
Drain to Source On-State Resistance	$R_{DS(on)}$	I_D = 6A, V_{GS} = 4.5V		18	24	mΩ
		I_D = 5A, V_{GS} = 2.5V		25	33	mΩ
Diode Forward Voltage	V_{SD}	I_S = 1.7A, V_{GS} = 0			1.4	V
Input Capacitance	C_{iss}	V_{GS}=0V, V_{DS}=8V, Frequency=1.0MHz		522		pF
Output Capacitance	C_{oss}			98.5		pF
Reverse Transfer Capacitance	C_{rss}			75		pF
Turn-ON Delay Time	$t_{d(on)}$	V_{DD} = 10V, V_{GS} = 4.5V, R_{GEN} = 3Ω, I_D=6A		10.5		ns
Turn-ON Rise Time	t_r			4.5		ns
Turn-OFF Delay Time	$t_{d(off)}$			27.5		ns
Turn-ON Fall Time	t_f			4		ns
Total Gate Charge	Q_g	V_{DS} = 10V, V_{GS} = 4.5V, I_D = 6A		6.5		nC
Gate-Source Charge	Q_{gs}			1.5		nC
Gate-Drain Charge	Q_{gd}			1.3		nC

Note 3: Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

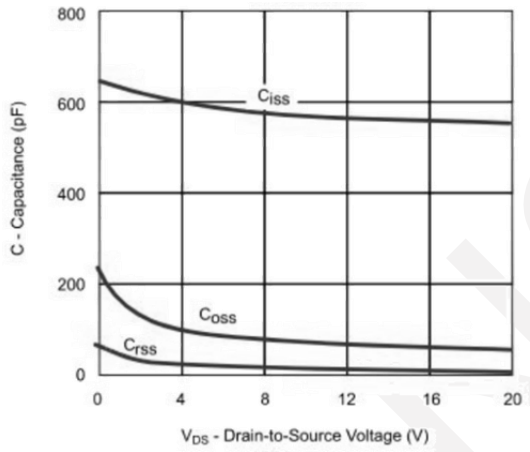
7.Typical electrical and thermal characteristics



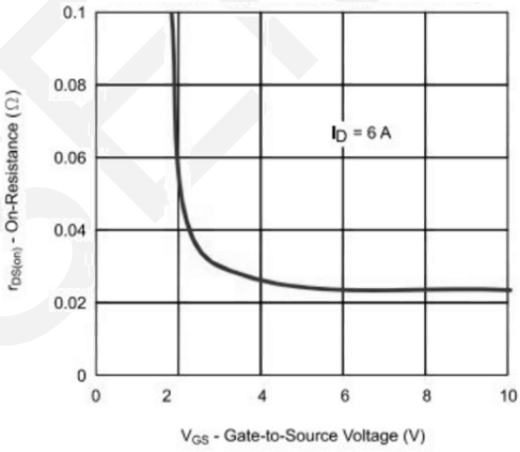
Transfer Characteristics



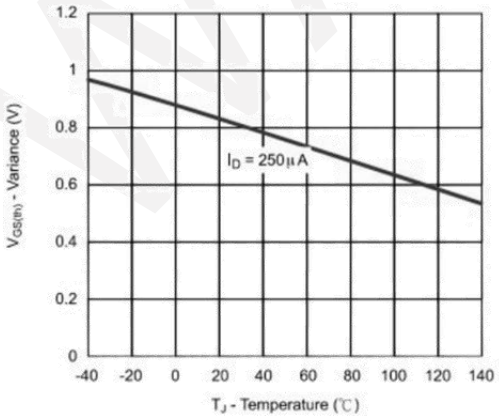
On-Resistance vs. Drain Current



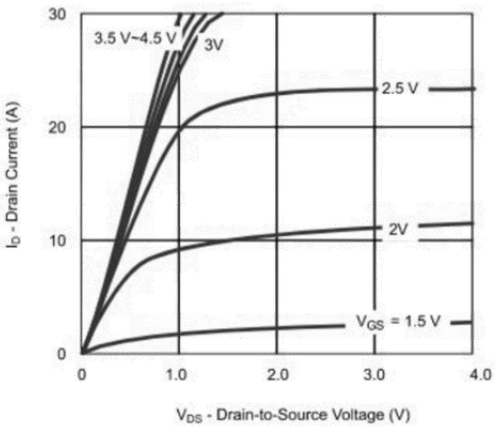
Capacitance



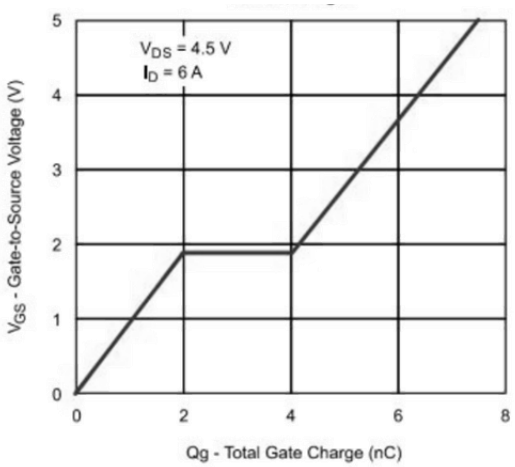
On-Resistance vs. Gate-to-Source Voltage



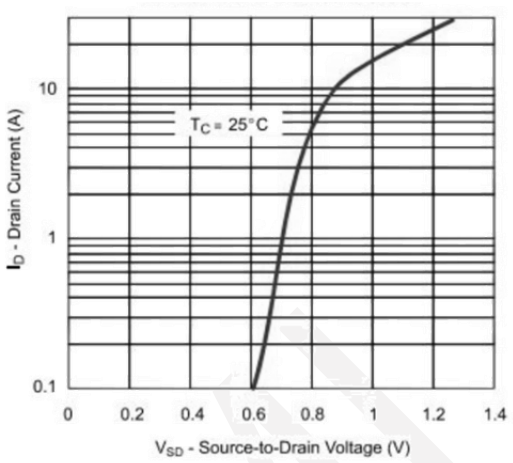
Threshold Voltage



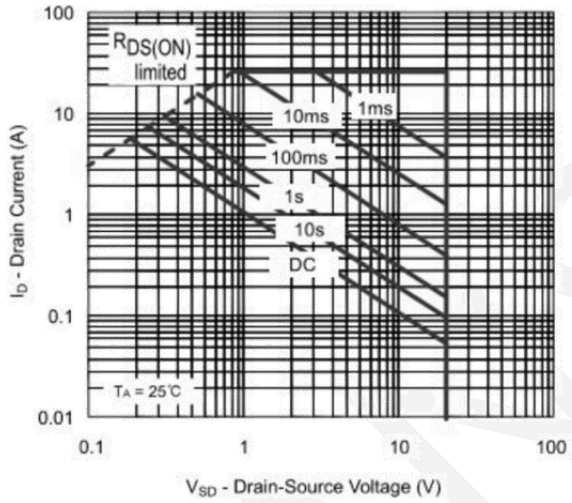
On-Region Characteristics



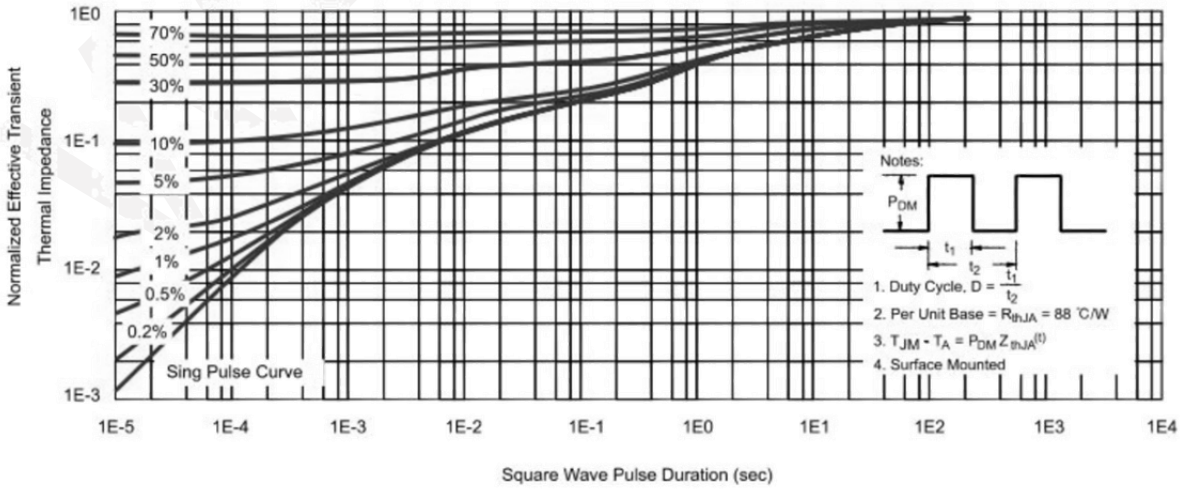
Gate Charge



On-Resistance vs. Drain Current

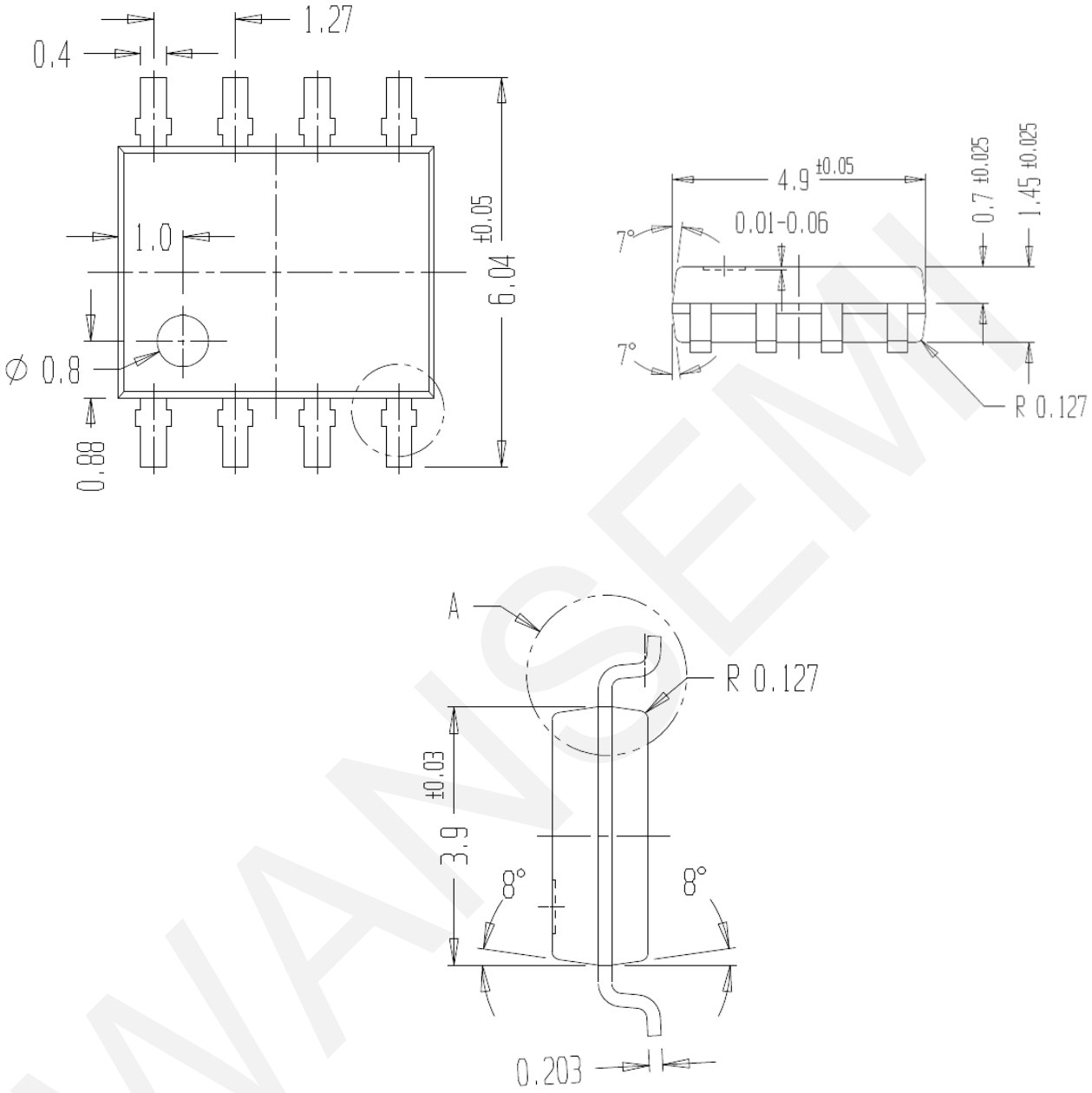


Maximum Forward Biased Safe Operating Area



Normalized Thermal Transient Impedance,Junction-to-Ambient

8.Package Dimensions



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