



**WANSEMI**  
万芯半导体

**WP3400CS3**

# **Enhancement Mode N-Channel Power MOSFET**

**SOT23-3/NMOS/30V/ $\pm 12$ V/0.9V/6.5A/17.2m $\Omega$**

**Rev1.2**

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## 30V, 17.2mΩ, 6.5A, Single N-Channel

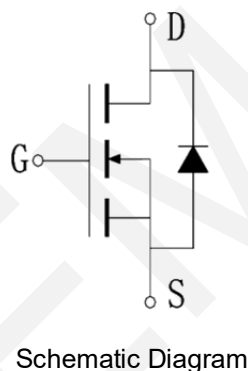
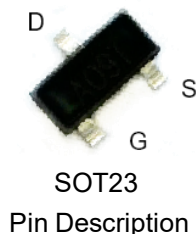
### 1.Features

- ◆ 30V MOSFET technology
- ◆ Low on-state resistance
- ◆ Fast switching
- ◆  $V_{GS} \pm 12V$

$V_{DS}$	$R_{DS(on)}$ Typ.	$I_D$ Max.
30V	17.2mΩ @ 10V	6.5A
	19mΩ @ 4.5V	
	24mΩ @ 2.5V	

### 2.Applications

- ◆ Power Switching Application
- ◆ Load Switching



### 3.Package Marking and Ordering Information

Part no.	Marking	Package	PCS/Reel	PCS/CTN.
WP3400CS3	*3400	SOT23-3	3,000	180,000

### 4.Absolute Max Ratings at Ta=25°C (Note1)

Parameter	Symbol	Maximum	Units
Drain to Source Voltage	$V_{DSS}$	30	V
Gate to Source Voltage	$V_{GSS}$	$\pm 12$	V
Drain Current (DC)	$I_D$	6.5	A
Drain Current (Pulse), $PW \leq 300\mu s$	$I_{DP}$	26	A
Total Dissipation	$P_D$	1.36	W
Junction Temperature	$T_j$	150	°C
Storage Temperature	$T_{stg}$	-55 to +150	°C

Note 1: Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

### 5. Thermal Resistance Ratings (Note 2)

Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	92	$^{\circ}\text{C/W}$

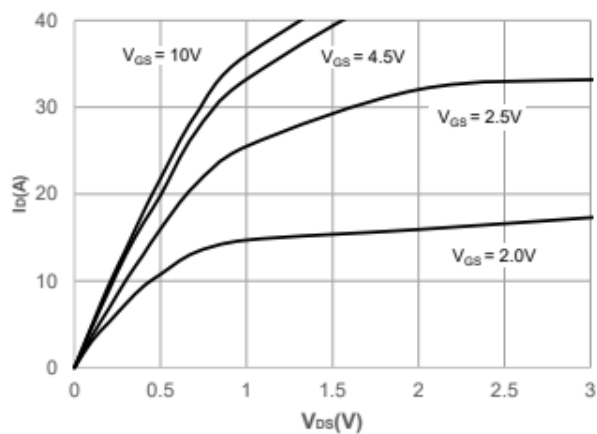
Note 2: When mounted on 1 inch square copper board  $t \leq 10\text{sec}$  The value in any given application depends on the user's specific board design.

### 6. Electrical Characteristics at $T_a=25^{\circ}\text{C}$ (Note 3)

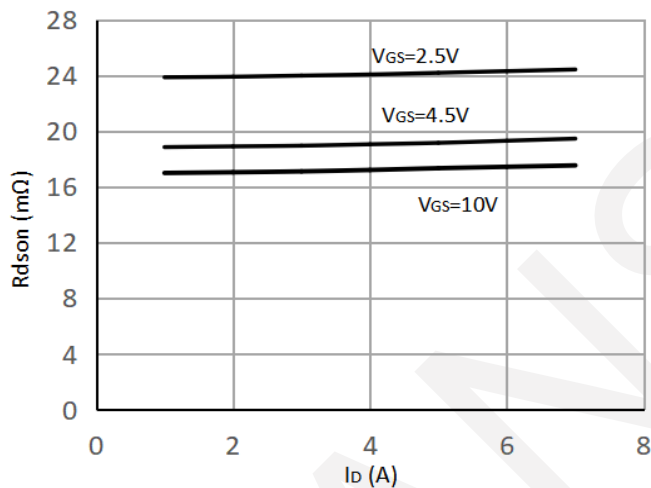
Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Units
Drain to Source Breakdown Voltage	$V_{(BR)DSS}$	$I_D = 250\mu\text{A}$ , $V_{GS} = 0\text{V}$	30	33	-	V
Zero-Gate Voltage Drain Current	$I_{DSS}$	$V_{DS} = 30\text{V}$ , $V_{GS} = 0\text{V}$	-	-	1	$\mu\text{A}$
Gate to Source Leakage Current	$I_{GSS}$	$V_{GS} = \pm 12\text{V}$ , $V_{DS} = 0\text{V}$	-	-	$\pm 100$	nA
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}$ , $I_{DS}=250\mu\text{A}$	0.5	0.9	1.5	V
Static Drain to Source On-State Resistance	$R_{DS(on)}$	$I_D = 5.5\text{A}$ , $V_{GS} = 10\text{V}$	-	17.2	22	$\text{m}\Omega$
		$I_D = 4.5\text{A}$ , $V_{GS} = 4.5\text{V}$	-	19	25	$\text{m}\Omega$
		$I_D = 3\text{A}$ , $V_{GS} = 2.5\text{V}$	-	24	35	$\text{m}\Omega$
Input Capacitance	$C_{iss}$	$V_{GS}=0\text{V}$ , $V_{DS}=15\text{V}$ , Frequency=1.0MHz	-	785	-	pF
Output Capacitance	$C_{oss}$		-	66	-	pF
Reverse Transfer Capacitance	$C_{rss}$		-	54	-	pF
Turn-ON Delay Time	$t_{d(on)}$	$V_{DS} = 15\text{V}$ , $I_D = 3\text{A}$ $V_{GS} = 10\text{V}$ , $R_G = 3\Omega$	-	4	-	ns
Rise Time	$t_r$		-	11	-	ns
Turn-OFF Delay Time	$t_{d(off)}$		-	24	-	ns
Fall Time	$t_f$		-	2	-	ns
Total Gate Charge	$Q_g$	$V_{DS} = 15\text{V}$ , $V_{GS} = 10\text{V}$ , $I_D = 3\text{A}$	-	19	-	nC
	$Q_{gs}$		-	2	-	nC
	$Q_{gd}$		-	2.1	-	nC
Diode Forward Voltage	$V_{FSD}$	$I_S = 6.5\text{A}$ , $V_{GS} = 0\text{V}$	0.5	0.85	1.2	V

Note 3: Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

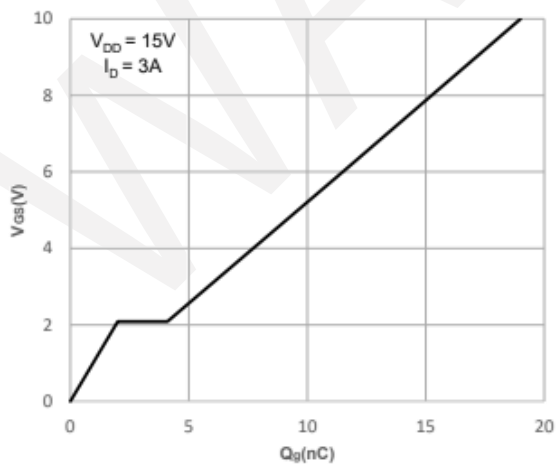
## 7. Typical Electrical and Thermal Characteristics



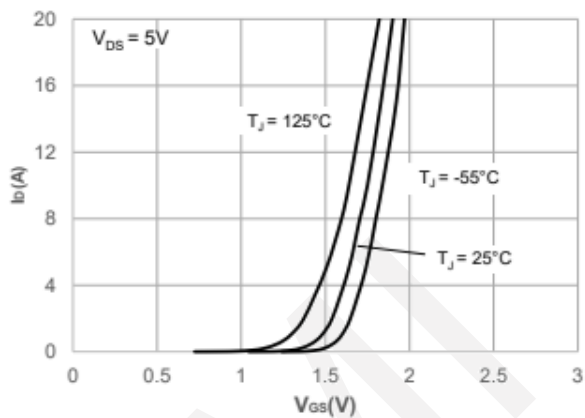
**Output Characteristics**



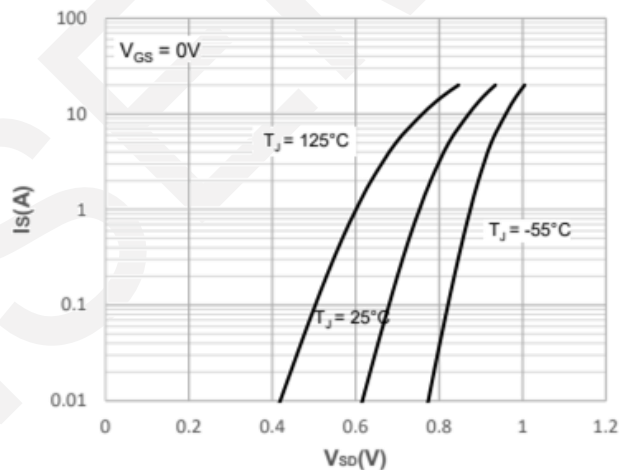
**On-resistance vs. Drain Current**



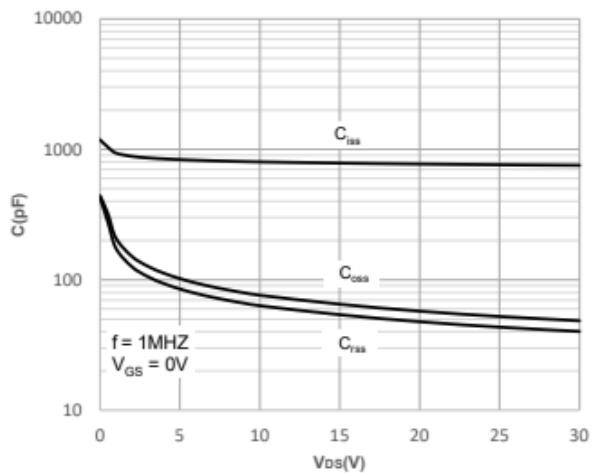
**Gate Charge Characteristics**



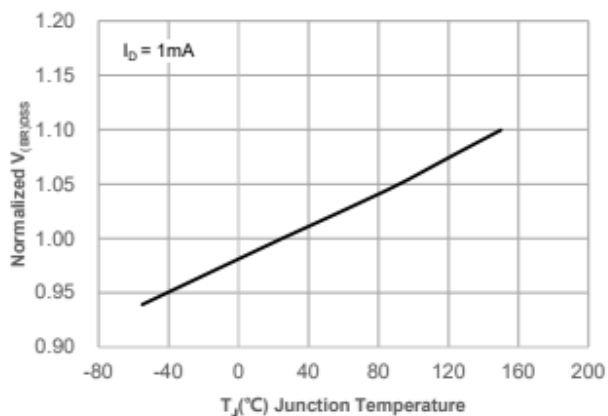
**Typical Transfer Characteristics**



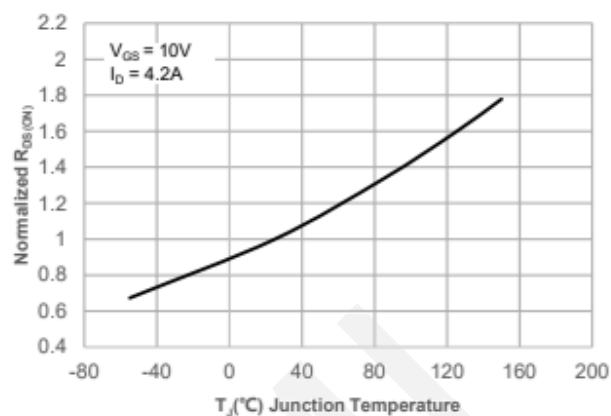
**Body Diode Characteristics**



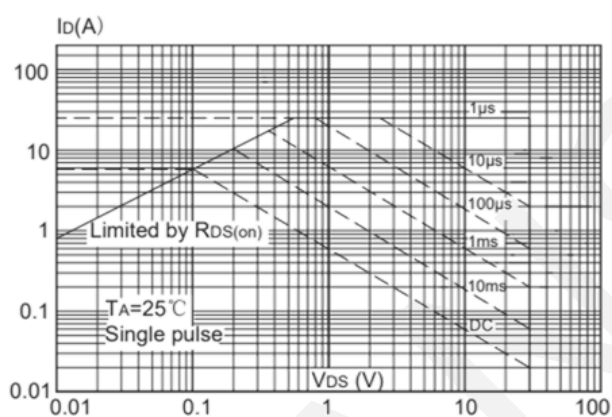
**Capacitance Characteristics**



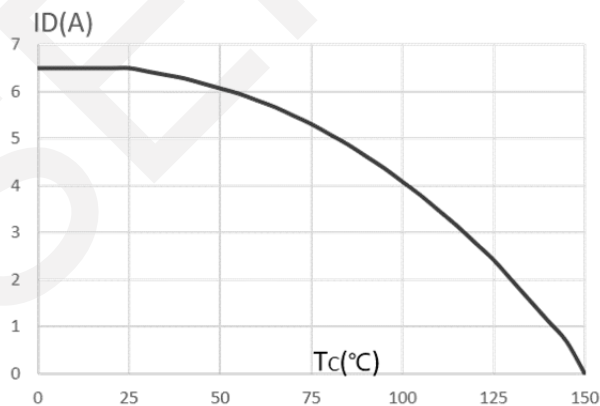
Normalized Breakdown Voltage vs.  
Junction Temperature



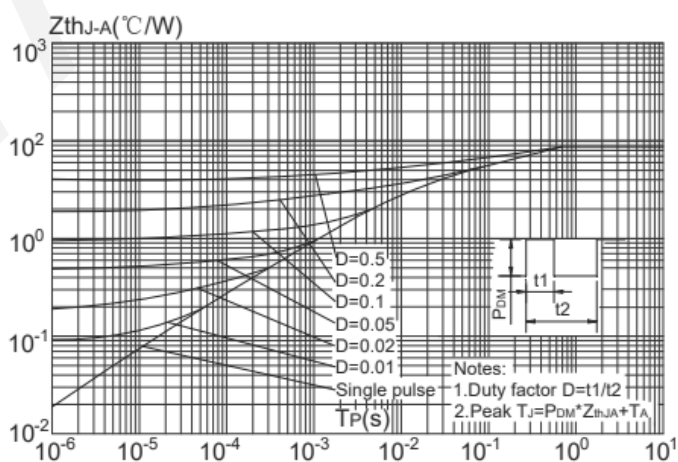
Normalized on Resistance vs.  
Junction Temperature



Maximum Safe Operating Area

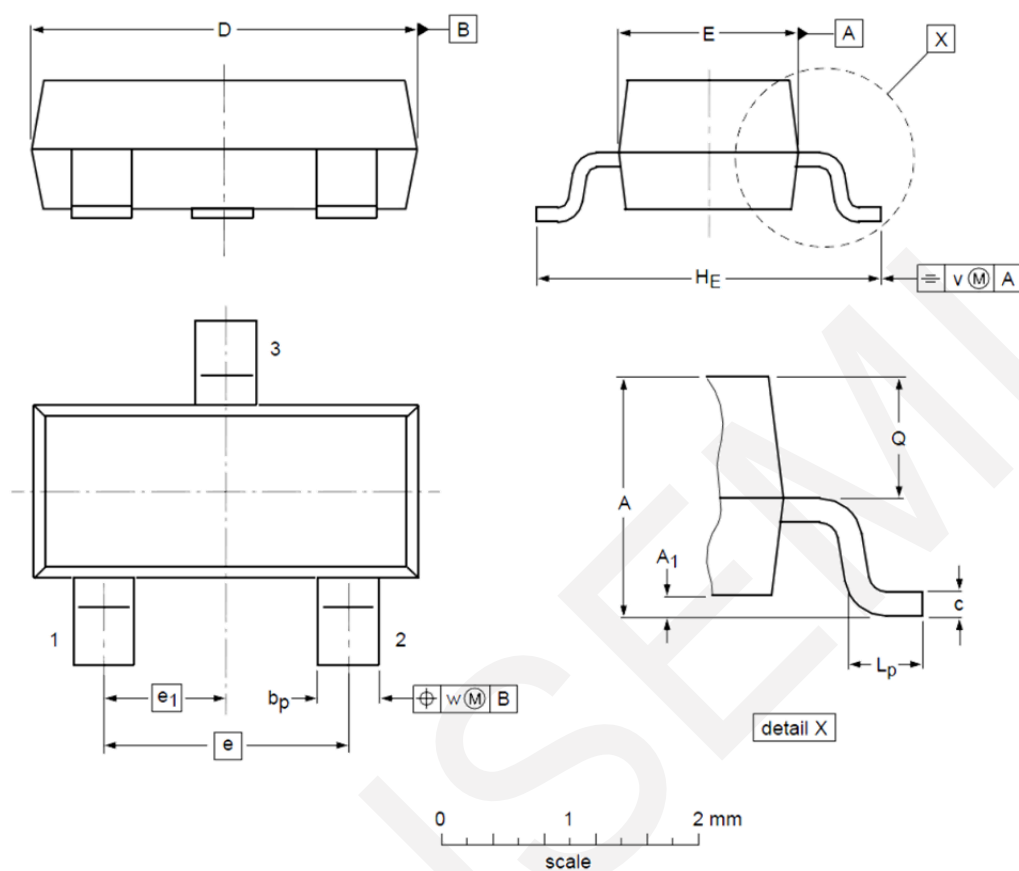


Maximum Continuous Drain Current vs.  
Case Temperature



Maximum Effective Transient  
Thermal Impedance, Junction-to-Case

## 8.Package Dimensions



**DIMENSIONS** ( unit : mm )

Symbol	Min	Typ	Max	Symbol	Min	Typ	Max
<b>A</b>	1.00	1.17	1.30	<b>A<sub>1</sub></b>	0.01	0.05	0.10
<b>b<sub>p</sub></b>	0.35	0.39	0.50	<b>c</b>	0.10	0.20	0.26
<b>D</b>	2.70	2.90	3.10	<b>E</b>	1.30	1.58	1.70
<b>e</b>	--	1.90	--	<b>e<sub>1</sub></b>	--	0.95	--
<b>H<sub>E</sub></b>	2.50	2.78	3.00	<b>L<sub>p</sub></b>	0.20	0.32	0.60
<b>Q</b>	0.23	0.27	0.33	<b>v</b>	--	0.20	--
<b>w</b>	--	0.20	--				

## **9. Important Notice**

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