

# **Enhancement Mode N-Channel Power MOSFET**

SOT23-3/NMOS/20V/ $\pm$ 12V/0.8V/4A/27m $\Omega$ 

Rev0.7





# 20V, 27mΩ, 4A, Single N-Channel

#### 1.Features

- ◆ 20V MOSFET technology
- ◆ Low on-state resistance
- Fast switching
- ♦ Vgs±12V

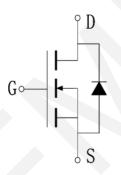
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- ◆ Power Switching Application
- Load Switching



Pin Description

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Schematic Diagram

## 3. Package Marking and Ordering Information

Part no.	Marking	Package	PCS/Reel	PCS/CTN.
WP2302A	A2sHB	SOT23-3	3,000	180,000

### 4.Absolute Max Ratings at Ta=25°C (Note1)

Parameter	Symbol	Maximum	Units
Drain to Source Voltage	V <sub>DSS</sub>	20	V
Gate to Source Voltage	$V_{GSS}$	±12	V
Drain Current (DC)	I <sub>D</sub>	4	А
Drain Current (Pulse), PW≤300µs	I <sub>DP</sub>	10.8	Α
Total Dissipation	$P_{D}$	0.9	W
Junction Temperature	Tj	150	°C
Storage Temperature	$T_{stg}$	-55 to +150	°C

Note 1: Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.



#### 5. Thermal Resistance Ratings

Parameter	Symbol	Value	Unit
Junction to Ambient (Note 2)	$R_{ hetaJA}$	125	°C/W

Note 2: When mounted on 1 inch square copper board  $t \le 10$ sec The value in any given application depends on the user's specific board design.

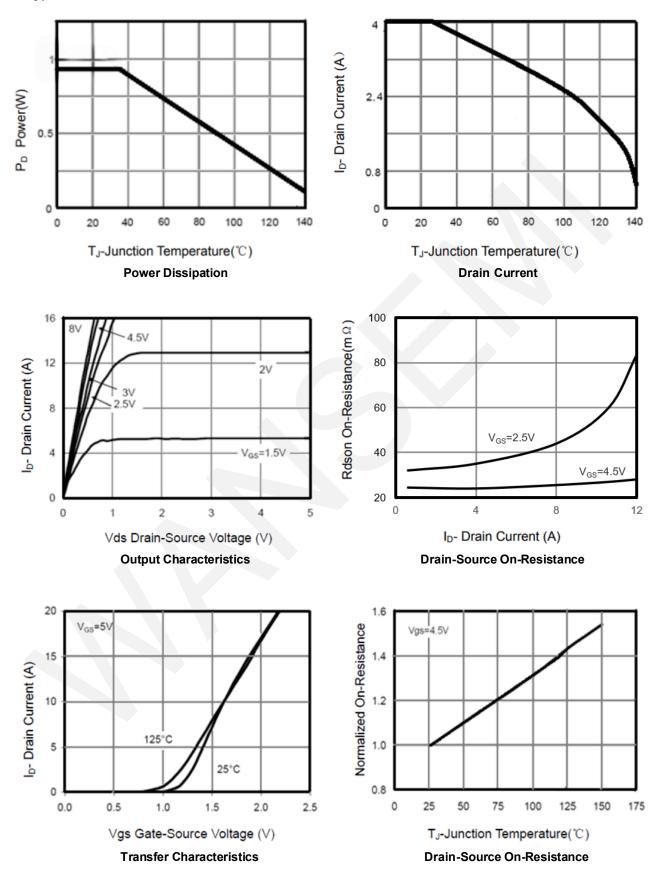
#### 6.Electrical Characteristics at Ta=25°C (Note 3)

Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Units
Drain to Source Breakdown Voltage	V <sub>(BR)DSS</sub>	$I_D = 250 \mu A, V_{GS} = 0 V$	20	22		V
Zero-Gate Voltage Drain Current	I <sub>DSS</sub>	$V_{DS} = 20V, V_{GS} = 0V$			1	μA
Gate to Source Leakage Current	I <sub>GSS1</sub>	$V_{GS} = \pm 12V, V_{SS} = 0V$			±100	nA
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}$ , $I_{DS}=250\mu A$	0.4	0.8	1	V
Static Drain to Source On-State	В	$I_D = 4A, V_{GS} = 4.5V$		27	35	mΩ
Resistance	R <sub>DS(on)</sub>	$I_D = 2A, V_{GS} = 2.5V$		37	50	mΩ
Input Capacitance	C <sub>iss</sub>	V <sub>GS</sub> =0V,		300		pF
Output Capacitance	$C_{oss}$	V <sub>DS</sub> =10V,		120		pF
Reverse Transfer Capacitance	C <sub>rss</sub>	Frequency=1.0MHz		80		pF
Turn-ON Delay Time	$t_{d(on)}$			10		ns
Rise Time	t <sub>r</sub>	$V_{DD} = 10V, I_D = 2.9A,$		50		ns
Turn-OFF Delay Time	$t_{d(off)}$	$V_{GEN} = 4.5V$ , $R_G = 6\Omega$		17		ns
Fall Time	t <sub>f</sub>			10		ns
	$Q_g$	V <sub>DS</sub> = 10V,		4		nC
Total Gate Charge	$Q_{gs}$	V <sub>GS</sub> = 4.5V,		0.65		nC
	$Q_{gd}$	I <sub>DS</sub> = 2.9A		1.2		nC
Diode Forward Voltage	$V_{FSD}$	I <sub>S</sub> = 2.9A, V <sub>GS</sub> = 0V		0.9	1.2	V

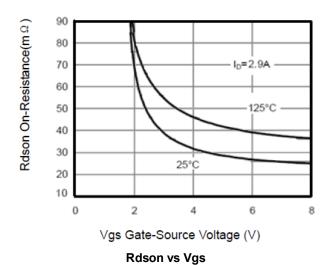
Note 3: Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

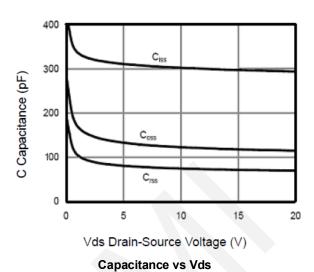


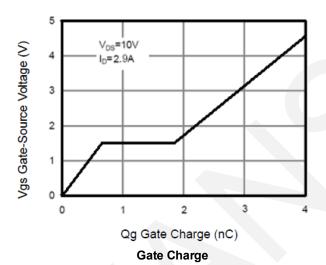
## 7. Typical electrical and thermal characteristics

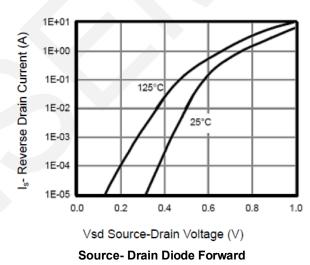


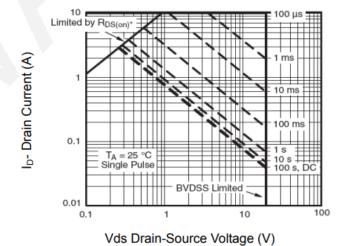






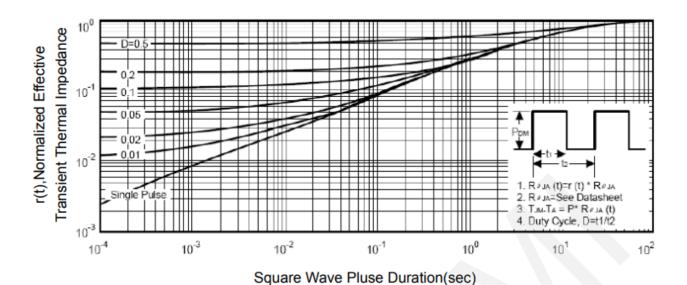






Safe Operation Area

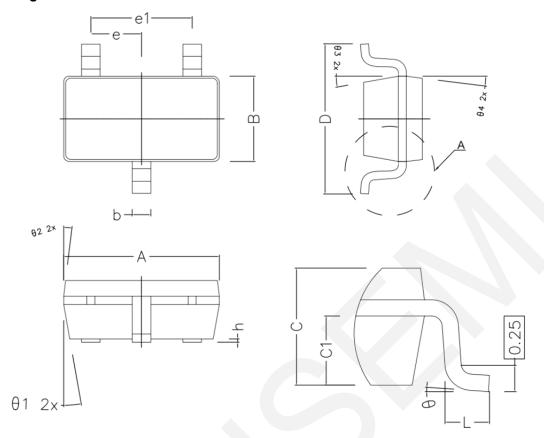




**Normalized Maximum Transient Thermal Impedance** 



# 8.Package Dimensions



COMMON DIMENSIONS (UNITS OF MEASURE IS mm)					
	MIN	NORMAL	MAX		
А	2.820	2.920	3.020		
В	1.500	1.600	1.700		
C	1.050	1.050 1.100 1.150			
C1	0.600	0.600 0.650 0.700			
D	2.650 2.800 2.950				
	0.300	0.300 0.450 0.60			
b	0.280	0.280 0.350			
h	0.020 0.050 0.100				
е	0.950TYPE				
e1	1.900TYPE				
θ1	10° TYPE				
θ2	7° TYPE				
θз	10° TYPE				
θ4	7° TYPE				
θ	0° ~ 8°				



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