

Enhancement Mode N-Channel Power MOSFET

 $TO-263/NMOS/30V/\pm20V/1.5V/150A/2.0m\Omega$

Rev_{0.6}





30V, 2.0m Ω , 150A, Single N-Channel

1.Features

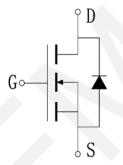
- ♦ 30V MOSFET technology
- ◆ Low on-state resistance
- Fast switching
- ♦ Vgs±20V

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- ◆ Power Switching Application
- Load Switching



V _{DS}	R _{DS(on)} Typ.	I _D Max.	
201/	2.0mΩ @ 10V	1504	
30V	2.7mΩ @ 4.5V	150A	



Schematic Diagram

3. Package Marking and Ordering Information

Part no.	Marking	Package	PCS/Reel	PCS/CTN.
WP30H50KKF	WP30H50K	TO-263	800	4,000

4.Absolute Max Ratings at Ta=25°C (Note1)

Parameter	Symbol	Maximum	Units
Drain to Source Voltage	V _{DSS}	30	V
Gate to Source Voltage	V_{GSS}	±20	V
Drain Current (DC)	I _D	150	А
Drain Current (Pulse), PW≤300μs	I _{DP}	600	А
Total Dissipation	P_{D}	347	W
Avalanche Energy, Single Pulsed	E _{AS}	400	mJ
Junction Temperature	Tj	150	°C
Storage Temperature	T _{stg}	-55 to +150	°C

Note 1: Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.



5. Thermal Resistance Ratings (Note 2)

Parameter	Symbol	Value	Unit
Junction to case	$R_{ heta JC}$	0.36	°C/W

Note 2: When mounted on 1 inch square copper board $t \le 10$ sec The value in any given application depends on the user's specific board design.

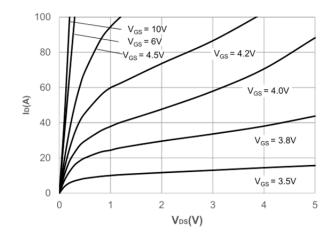
6.Electrical Characteristics at Ta=25°C (Note 3)

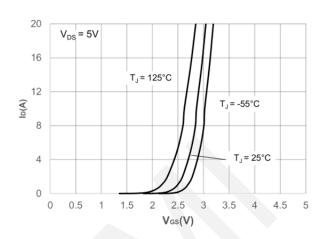
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Units
Drain to Source Breakdown Voltage	V _{(BR)DSS}	$I_D = 250 \mu A, V_{GS} = 0 V$	30	-	-	V
Zero-Gate Voltage Drain Current	I _{DSS}	V_{DS} =30V, V_{GS} = 0V	-	-	1	μA
Gate to Source Leakage Current	I _{GSS}	$V_{GS} = \pm 20V, V_{DS} = 0V$	ı	-	±100	nA
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}$, $I_{DS}=250\mu A$	1.0	1.5	2.5	V
Static Drain to Source On-State	D	$I_D = 30A, V_{GS} = 10V$		2.0	3.2	mΩ
Resistance	R _{DS(on)}	$I_D = 20A, V_{GS} = 4.5V$	-	2.7	5.3	mΩ
Input Capacitance	C _{iss}	V _{GS} =0V,	-	5691	-	pF
Output Capacitance	C _{oss}	V _{DS} =15V,	-	494	-	pF
Reverse Transfer Capacitance	C _{rss}	Frequency=1.0MHz	1	366	-	pF
Turn-ON Delay Time	t _{d(on)}		ı	10	-	ns
Rise Time	t _r	$V_{DD} = 15V, I_D = 30A,$	-	19	-	ns
Turn-OFF Delay Time	$t_{\text{d(off)}}$	$V_{GS} = 10V,$ $R_{GEN} = 3\Omega$	-	50	-	ns
Fall Time	t _f		-	20	-	ns
	Q_g	V _{DD} = 15V,	-	67	-	nC
Total Gate Charge	Q_{gs}	V _{GS} = 10V, I _D = 30A	-	11	-	nC
	Q_{gd}		-	19	-	nC
Diode Forward Voltage	V_{FSD}	I _S =30A, V _{GS} = 0V	-	-	1.2	V

Note 3: Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

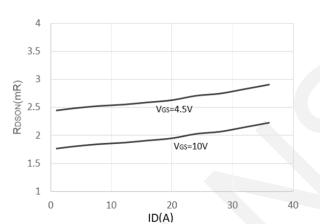


7. Typical electrical and thermal characteristics

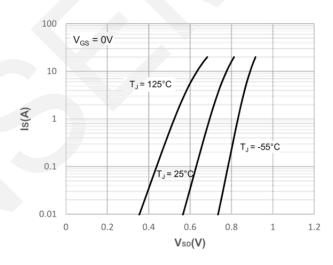




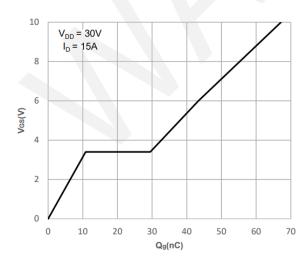
Output Characteristics



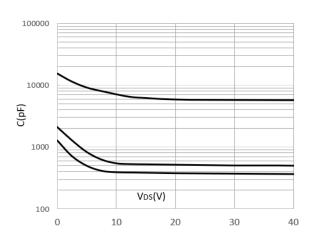
Typical Transfer Characteristics



On-resistance vs. Drain Current



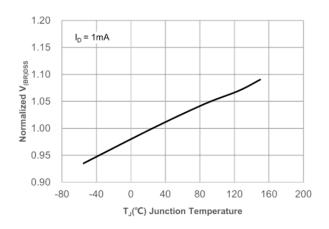
Body Diode Characteristics



Gate Charge Characteristics

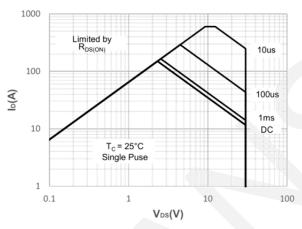
Capacitance Characteristics



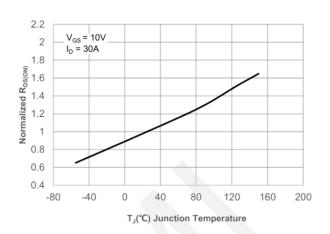


Normalized Breakdown Voltage vs.

Junction Temperature

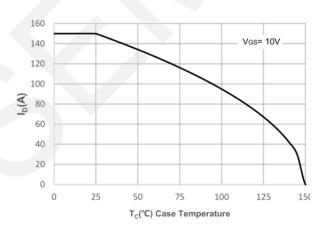


Maximum Safe Operating Area



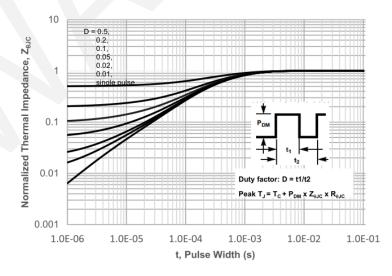
Normalized on Resistance vs.

Junction Temperature



Maximum Continuous Drain Current vs.

Case Temperature

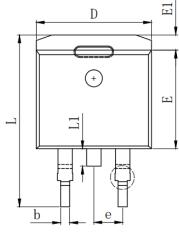


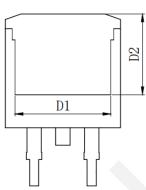
Maximum Effective Transient

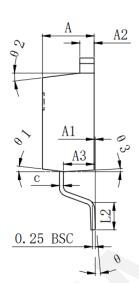
Thermal Impedance, Junction-to-Case WAN SEMICONDUCTOR (NINGBO) CO.,LTD

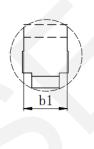


8.Package Dimensions









SYMBOL	MILLIMETER			
SIMDUL	MIN Typ.		MAX	
A	4. 370	4.770		
A1	0.000		0. 250	
A2	1. 220	1. 270	1. 420	
A3	2, 490	2.690	2.890	
b	0. 700	0.810	0.960	
b1	1.170	1. 270	1. 470	
с	0.300	0.380	0. 530	
D	9.860	10.160	10.360	
D1	8. 400 REF			
D2	7.073 REF			
Е	8. 500	8. 900		
El	1.070	1. 470		
е		2.540 TYP		
L	14.700	15. 100	15.500	
L1	1.400	1.550	1.700	
L2	2.000	2.600		
θ	0°		9°	
θ 1	7° TYP			
θ2	7° TYP			
θ 3	3° TYP			



9. Important Notice

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