

Enhancement Mode N-Channel Power MOSFET

PDFN5X6/NMOS/40V/ \pm 20V/1.5V/150A/2.2m Ω

Rev_{0.1}





40V, 2.2mΩ, 150A, N-Channel Enhancement MOSFET

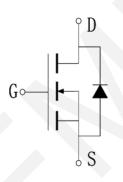
1.Features

- ♦ 40V MOSFET technology
- ◆ Low on-state resistance
- Fast switching
- ♦ Vgs±20V
- ◆ 100% RG Tested
- ◆ 100% UIS Tested

- Power Switching Application
- ◆ Load Switching



V _{DS}	R _{DS(on)} Typ.	I _D Max.	
40V	2.2mΩ @ 10V	4504	
	2.8mΩ @ 4.5V	150A	



Schematic Diagram

3. Package Marking and Ordering Information

Part no.	Marking	Package	PCS/Reel	PCS/CTN.
WP40H50KPA	40H50KPA	PDFN5X6	5,000	50,000

4.Absolute Max Ratings at Ta=25°C (Note1)

Parameter	Symbol	Maximum	Units
Drain to Source Voltage	V_{DSS}	40	V
Gate to Source Voltage	V_{GSS}	±20	V
Drain Current (DC)	I _D	150	Α
Drain Current (Pulse), PW≤300μs	I _{DP}	600	А
Total Dissipation	P_{D}	66	W
Avalanche Energy, Single Pulsed	E _{AS}	126	mJ
Junction Temperature	T _j	150	°C
Storage Temperature	T _{stg}	-55 to +150	°C

Note 1: Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.



5. Thermal Resistance Ratings (Note 2)

Parameter	Symbol	Value	Unit
Junction to Case	R _{eJC}	1.6	°C/W

Note 2: When mounted on 1 inch square copper board $t \le 10$ sec The value in any given application depends on the user's specific board design.

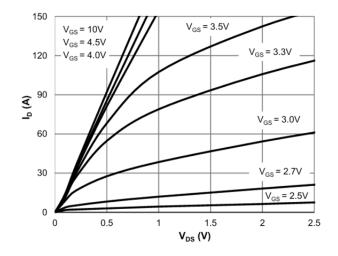
6.Electrical Characteristics at Ta=25°C (Note 3)

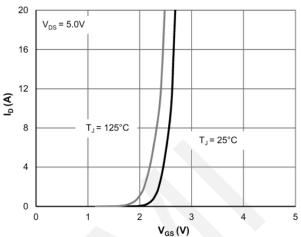
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Units
Drain to Source Breakdown Voltage	V _{(BR)DSS}	$I_D = 250 \mu A, V_{GS} = 0 V$	40	-	-	٧
Zero-Gate Voltage Drain Current	I _{DSS}	$V_{DS} = 40V, V_{GS} = 0V$	-	-	1	μA
Gate to Source Leakage Current	I _{GSS}	$V_{GS} = \pm 20V, V_{SS} = 0V$	ı	-	±100	nA
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}$, $I_{DS}=250\mu A$	1.0	1.5	2.5	>
Static Drain to Source On-State	D	$I_D = 20A, V_{GS} = 10V$	ı	2.2	2.8	mΩ
Resistance	R _{DS(on)}	$I_D = 15A, V_{GS} = 4.5V$	-	2.8	3.5	mΩ
Input Capacitance	C _{iss}	V _{GS} =0V,	-	14700	-	pF
Output Capacitance	C _{oss}	$V_{DS}=20V$,	-	843	-	pF
Reverse Transfer Capacitance	C _{rss}	Frequency=1.0MHz	1	830	ı	pF
Turn-ON Delay Time	t _{d(on)}	$V_{DD} = 20V,$ $R_L = 1.0\Omega,$	ı	5.5	ı	ns
Rise Time	t _r		-	8.7	-	ns
Turn-OFF Delay Time	$t_{\text{d(off)}}$	V _{GS} = 10V,	-	31	-	ns
Fall Time	t _f	$R_{GEN} = 6\Omega$	-	15.8	-	ns
	Q_g	V _{DS} = 20V,	-	36	-	nC
Total Gate Charge	Q_{gs}	V_{GS} =0 to 10V,	-	4.7	-	nC
	Q_{gd}	I _D = 20A	-	7.1	-	nC
Diode Forward Voltage	V_{FSD}	I _S = 20A, V _{GS} = 0V	0.5	-	1.2	V

Note 3: Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.



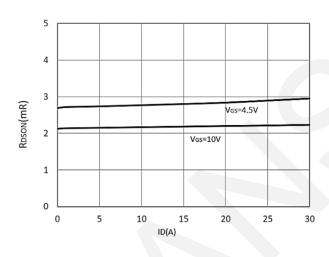
7. Typical electrical and thermal characteristics

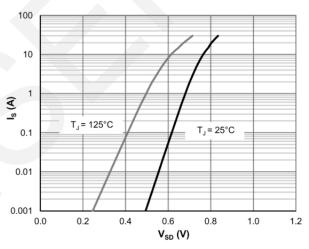




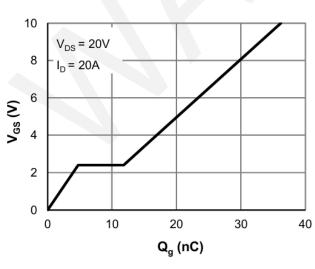
Output Characteristics



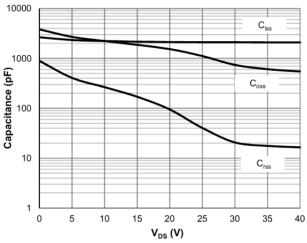




On-resistance vs. Drain Current



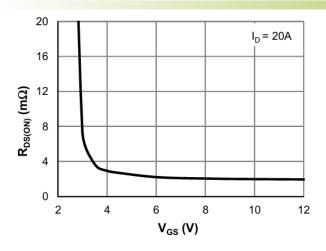
Body Diode Characteristics

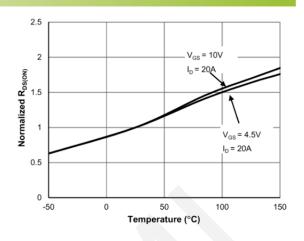


Gate Charge Characteristics

Capacitance Characteristics

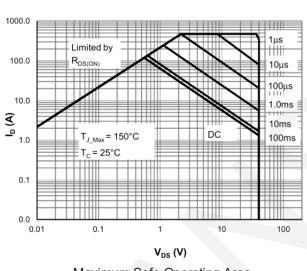


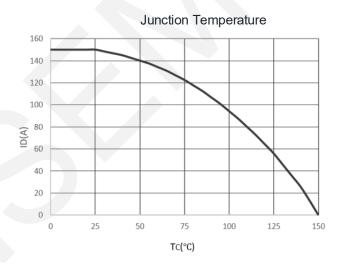




 $R_{\text{DS (ON)}} \ VS.V_{\text{GS}}$

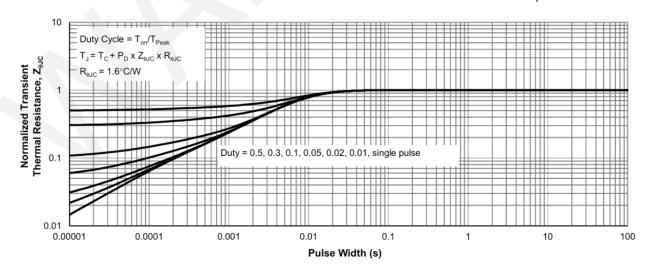
Normalized on Resistance vs.





Maximum Safe Operating Area

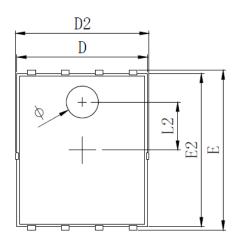
Maximum Continuous Drain Current vs.Case Temperature

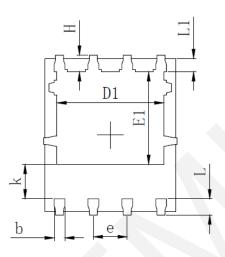


Maximum Effective Transient Thermal Impedance, Junction-to-Case



8.Package Dimensions





CUMPAT		MILLIMETER			
SYMBOL	MIN	Тур.	MAX		
A	0. 900	1. 000	1. 100		
A1		0. 254 REF.			
A2		0~0.05			
D	4.824	4. 900	4. 976		
D1	3. 910	4. 010	4. 110		
D2	4. 924	5. 000	5. 076		
Е	5. 924	6. 000	6. 076		
E1	3. 375	3. 475	3. 575		
E2	5. 674	5. 750	5. 826		
b	0.350	0. 400	0. 450		
е	1.270 TYP.				
L	0. 534	0. 610	0. 686		
L1	0.424	0. 500	0. 576		
L2	1.800 REF.				
k	1. 190	1. 290	1. 390		
Н	0. 549	0. 625	0. 701		
θ	8°	10°	12°		
ф	1. 100	1. 200	1.300		
d			0. 100		



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