

Enhancement Mode N-Channel Power MOSFET

PDFN5X6/NMOS/40V/ \pm 20V/1.8V/70A/4.8m Ω

Rev0.1





40V, 4.8mΩ, 70A, N-Channel Enhancement MOSFET

1.Features

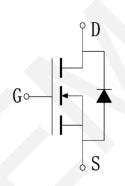
- ♦ 40V MOSFET technology
- ◆ Low on-state resistance
- ◆ Fast switching
- ♦ Vgs±20V
- ♦ 100% RG Tested
- ◆ 100% UIS Tested

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- ◆ Power Switching Application
- Load Switching



V _{DS}	R _{DS(on)} Typ.	I _D Max.	
40) (4.8mΩ @ 10V	70.4	
40V	6.8mΩ @ 4.5V	70A	



Schematic Diagram

3. Package Marking and Ordering Information

Part no.	Marking	Package	PCS/Reel	PCS/CTN.
WX048N04PA	048N04	PDFN5X6	5,000	50,000

4.Absolute Max Ratings at Ta=25°C (Note1)

Parameter	Symbol	Maximum	Units
Drain to Source Voltage	V _{DSS}	40	V
Gate to Source Voltage	V _{GSS}	±20	V
Drain Current (DC)	ID	70	А
Drain Current (Pulse), PW≤300μs	I _{DP}	280	А
Total Dissipation	P _D	60	W
Avalanche Energy, Single Pulsed	E _{AS}	121	mJ
Junction Temperature	Tj	150	°C
Storage Temperature	T_{stg}	-55 to +150	°C

Note 1: Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.



5. Thermal Resistance Ratings

Parameter	Symbol	Value	Unit
Junction to Case	Rejc	2.1	°C/W

Note 2: When mounted on 1 inch square copper board $t \le 10$ sec The value in any given application depends on the user's specific board design.

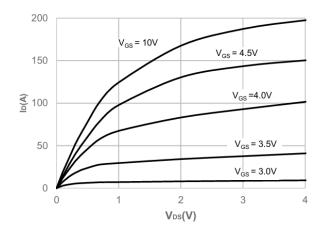
6.Electrical Characteristics at Ta=25°C (Note 3)

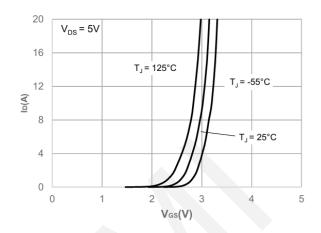
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Units
Drain to Source Breakdown Voltage	V _{(BR)DSS}	$I_D = 250 \mu A$, $V_{GS} = 0 V$	40	-	-	V
Zero-Gate Voltage Drain Current	I _{DSS}	V _{DS} =40V, V _{GS} = 0V	-		1	μΑ
Gate to Source Leakage Current	Igss	$V_{GS} = \pm 20V, V_{DS} = 0V$	-	-	±100	nA
Gate Threshold Voltage	$V_{GS(th)}$	V _{DS} =V _{GS} , I _{DS} =250µA	1.2	1.8	2.3	V
Static Drain to Source On-State	Б	I _D =30A, V _{GS} = 10V	1	4.8	6.0	mΩ
Resistance	R _{DS(on)}	I _D =20A, V _{GS} = 4.5V		6.8	8.8	mΩ
Input Capacitance	C _{iss}	V _{GS} =0V,	-	3031	-	pF
Output Capacitance	Coss	V _{DS} =20V,	-	213	-	pF
Reverse Transfer Capacitance	Crss	Frequency=1.0MHz	-	179	-	pF
Turn-ON Delay Time	t _{d(on)}		ı	11	-	ns
Rise Time	tr	$V_{DD} = 20V, V_{GS} = 10V,$	-	32	-	ns
Turn-OFF Delay Time	$t_{\sf d(off)}$	$R_{GEN} = 3\Omega, I_D = 30A$	-	52	-	ns
Fall Time	t _f		1	13	-	ns
	Q_g $V_{DS} = 20V$,		-	59	-	nC
Total Gate Charge	Qgs	$V_{GS} = 0 \text{ to } 10V,$ $I_{D} = 30A$	-	12	-	nC
	Q _{gd}		-	12	-	nC
Diode Forward Voltage	V_{FSD}	I _S =30A, V _{GS} = 0	-	-	1.2	V

Note 3: Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

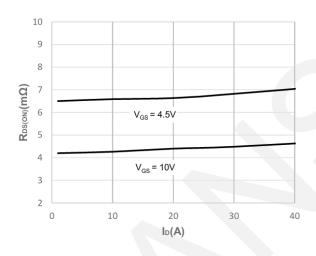


7. Typical electrical and thermal characteristics

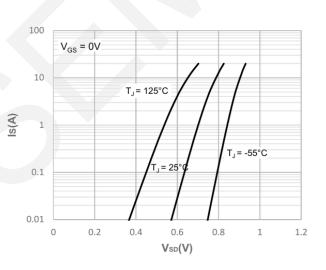




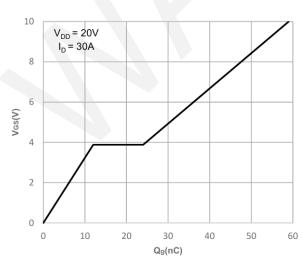
Output Characteristics



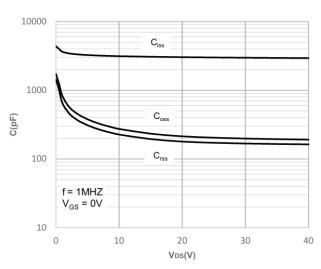
Typical Transfer Characteristics



On-resistance vs. Drain Current



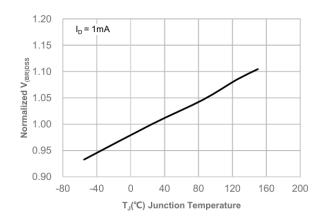
Body Diode Characteristics

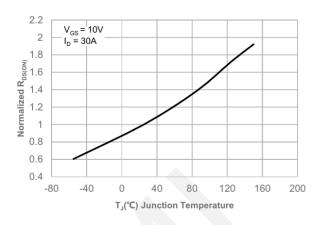


Gate Charge Characteristics

Capacitance Characteristics





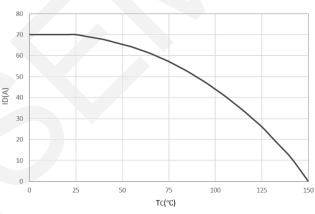


Normalized Breakdown voltage vs. Junction Temperature

1000 10us Limited by RDS(ON) 100 100us l_D(A) 10 1ms DC 1 T_C = 25°C Single Puse 0.1 1 100 0.1 10 V_{DS}(V)

Normalized on Resistance vs.

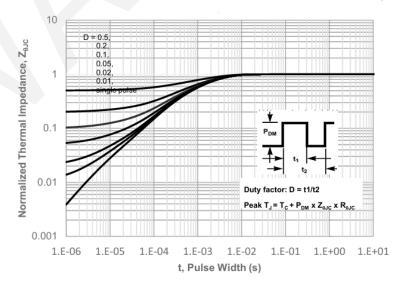




Maximum Safe Operating Area

Maximum Continuous Drain Current vs.

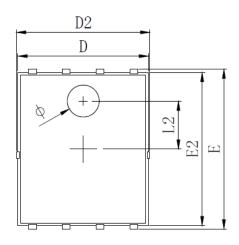
Case Temperature

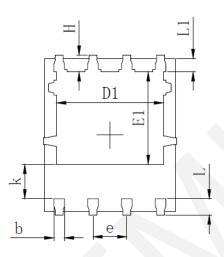


Maximum Effective Transient Thermal Impedance, Junction-to-Case



8.Package Dimensions





SYMBOL	MILLIMETER			
STMDOL	MIN	Тур.	MAX	
Α	0. 900	1.000	1. 100	
A1		0. 254 REF.		
A2		0~0.05		
D	4. 824	4. 900	4. 976	
D1	3. 910	4. 010	4. 110	
D2	4. 924	5. 000	5. 076	
E	5. 924	6. 000	6. 076	
E1	3. 375	3. 475	3. 575	
E2	5. 674	5. 750	5. 826	
ъ	0. 350	0. 400	0. 450	
e	1.270 TYP.			
L	0. 534	0. 610	0.686	
L1	0. 424	0. 500	0. 576	
L2	1.800 REF.			
k	1. 190	1. 290	1. 390	
Н	0. 549	0. 625	0. 701	
θ	8°	10°	12°	
ф	1. 100	1. 200	1.300	
d			0. 100	



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