



WANSEMI
万晶半导体

WX075N10PA

Enhancement Mode N-Channel Power MOSFET

PDFN5X6/NMOS/100V/±20V/1.7V/75A/7.5mΩ

Rev0.1



100V, 7.5mΩ, 75A, N-Channel MOSFET

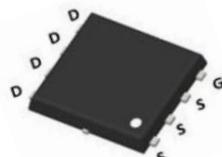
1. Features

- ◆ 60V MOSFET technology
- ◆ Low on-state resistance
- ◆ Fast switching
- ◆ $V_{GS} \pm 20V$
- ◆ 100% RG Tested
- ◆ 100% UIS Tested

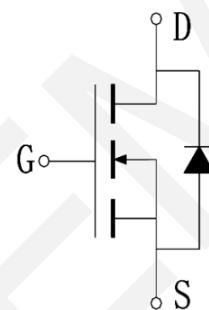
V_{DS}	$R_{DS(on)}$ Typ.	I_D Max.
100V	7.5mΩ @ 10V	75A
	9.5mΩ @ 4.5V	

2. Applications

- ◆ Power Switching Application
- ◆ Load Switching



PDFN5X6
Pin Description



Schematic Diagram

3. Package Marking and Ordering Information

Part no.	Marking	Package	PCS/Reel	PCS/CTN.
WX075N10PA	075N10	PDFN5X6	5,000	50,000

4. Absolute Max Ratings at $T_a=25^\circ C$ (Note1)

Parameter	Symbol	Maximum	Units
Drain to Source Voltage	V_{DSS}	100	V
Gate to Source Voltage	V_{GSS}	± 20	V
Drain Current (DC)	I_D	75	A
Drain Current (Pulse), $PW \leq 300\mu s$	I_{DP}	300	A
Total Dissipation	P_D	32	W
Avalanche Energy, Single Pulsed	E_{AS}	90	mJ
Junction Temperature	T_j	150	°C
Storage Temperature	T_{stg}	-55 to +150	°C

Note 1: Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.



5.Thermal Resistance Ratings

Parameter	Symbol	Value	Unit
Junction to case	R_{AJC}	3.9	°C/W

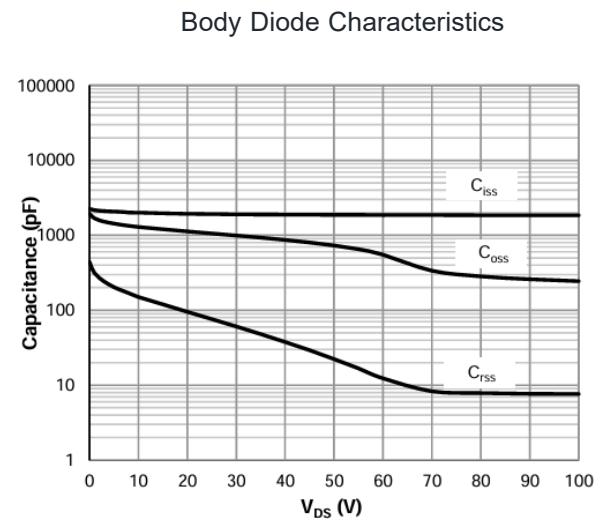
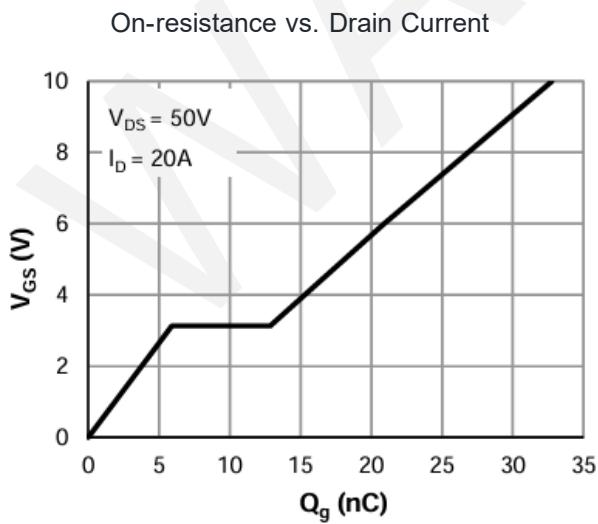
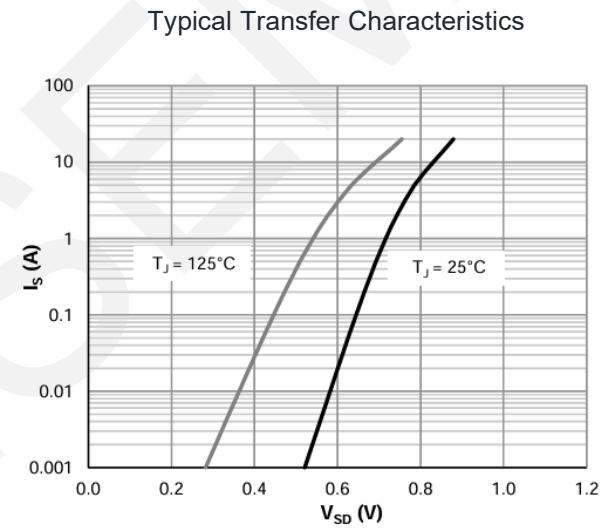
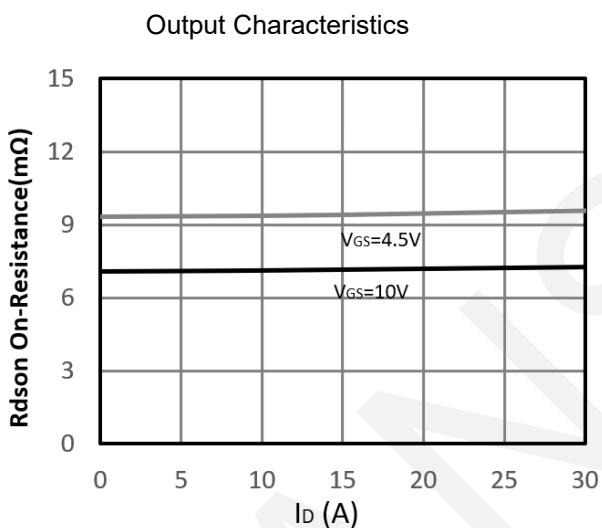
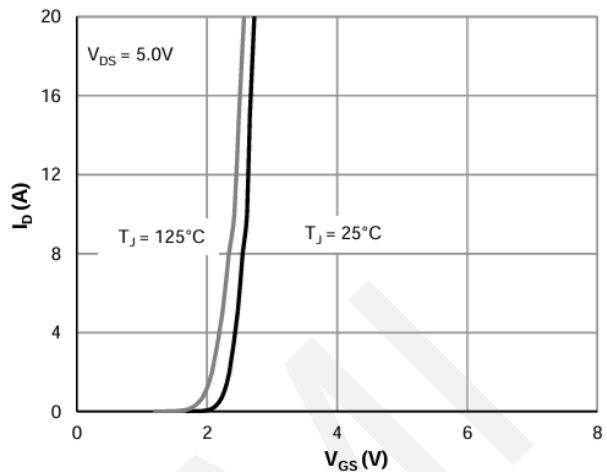
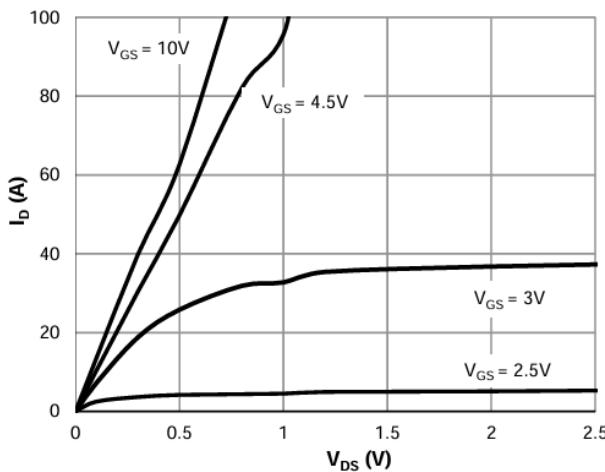
Note 2: When mounted on 1 inch square copper board $t \leq 10\text{sec}$ The value in any given application depends on the user's specific board design.

6.Electrical Characteristics at $T_a=25^\circ\text{C}$ (Note 3)

Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Units
Drain to Source Breakdown Voltage	$V_{(BR)DSS}$	$I_D = 250\mu\text{A}, V_{GS} = 0\text{V}$	100	-	-	V
Zero-Gate Voltage Drain Current	I_{DSS}	$V_{DS} = 100\text{V}, V_{GS} = 0\text{V}$	-	-	1	μA
Gate to Source Leakage Current	I_{GSS1}	$V_{GS} = \pm 20\text{V}, V_{DS} = 0\text{V}$	-	-	± 100	nA
Gate Threshold Voltage	$V_{GS(\text{th})}$	$V_{DS}=V_{GS}, I_{DS}=250\mu\text{A}$	1.2	1.7	2.4	V
Static Drain to Source On-State Resistance	$R_{DS(\text{on})}$	$I_D = 20\text{A}, V_{GS} = 10\text{V}$	-	7.5	9.0	$\text{m}\Omega$
		$I_D = 15\text{A}, V_{GS} = 4.5\text{V}$	-	9.5	13	$\text{m}\Omega$
Input Capacitance	C_{iss}	$V_{GS}=0\text{V}, V_{DS}=50\text{V},$ Frequency=1.0MHz	-	1872	-	pF
Output Capacitance	C_{oss}		-	731	-	pF
Reverse Transfer Capacitance	C_{rss}		-	22	-	pF
Turn-ON Delay Time	$t_{d(\text{on})}$	$V_{DD} = 50\text{V}, V_{GS} = 10\text{V},$ $R_{\text{GEN}} = 3\Omega, R_L = 2.5\Omega$	-	10	-	ns
Rise Time	t_r		-	20	-	ns
Turn-OFF Delay Time	$t_{d(\text{off})}$		-	40	-	ns
Fall Time	t_f		-	54	-	ns
Total Gate Charge	Q_g	$V_{DS} = 50\text{V},$ $V_{GS} = 0 \text{ to } 10\text{V},$ $I_D = 20\text{A}$	-	33	-	nC
	Q_{gs}		-	6	-	nC
	Q_{gd}		-	7	-	nC
Diode Forward Voltage	V_{FSD}	$I_S = 20\text{A}, V_{GS} = 0$	-	-	1.2	V

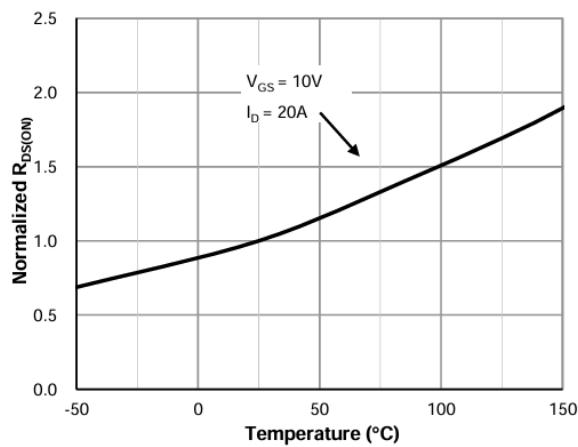
Note 3: Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

7.Typical electrical and thermal characteristics

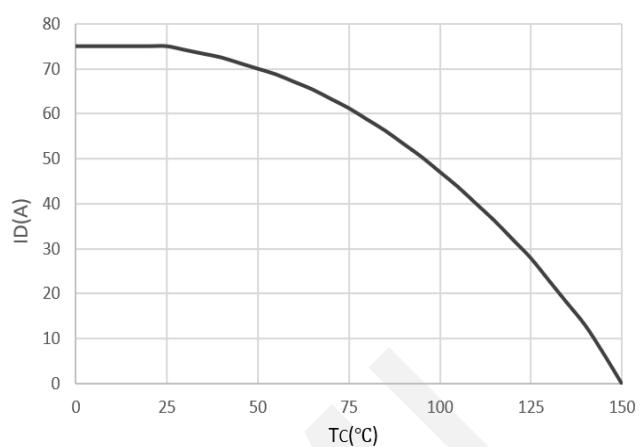


Gate Charge Characteristics

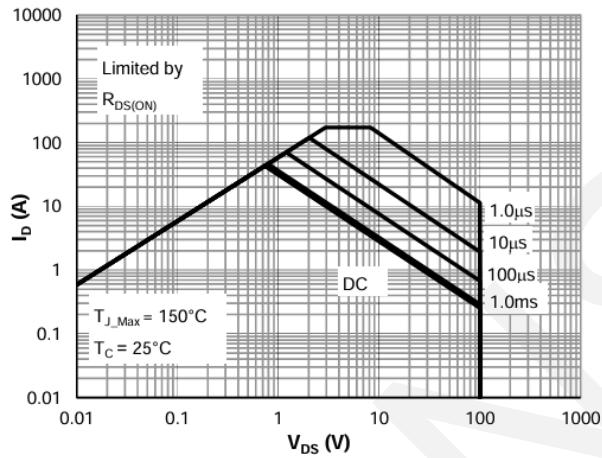
Capacitance Characteristics



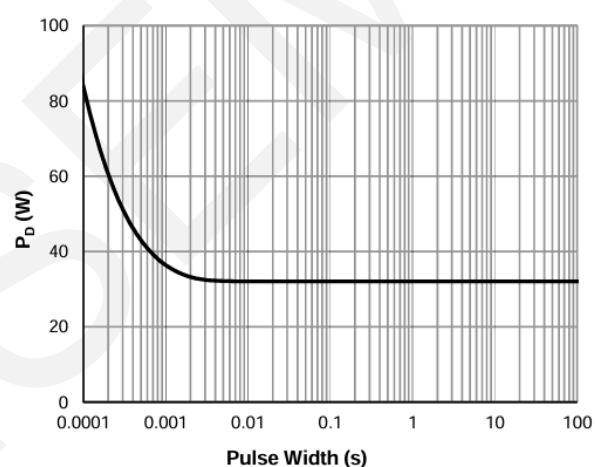
Normalized on Resistance vs.
Junction Temperature



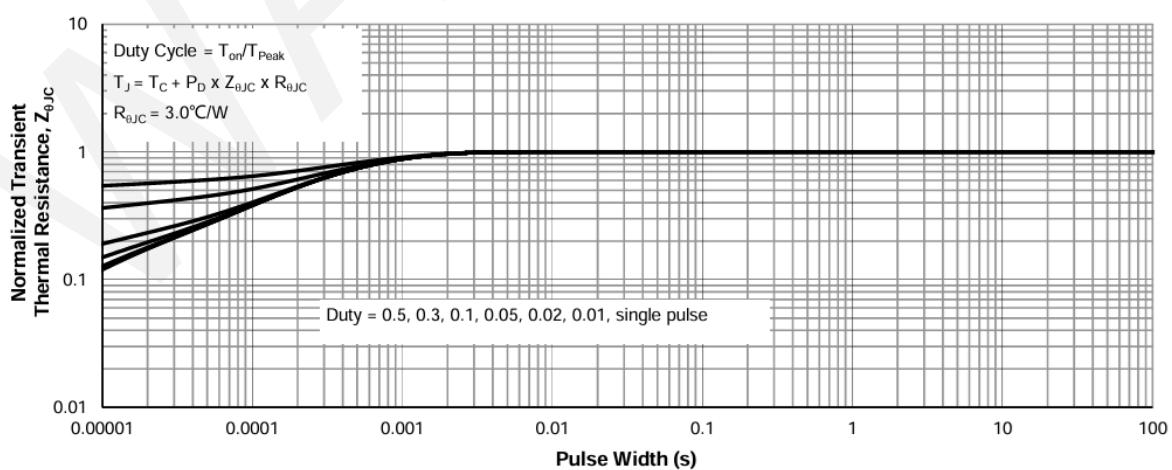
Maximum Continuous Drain Current vs.
Case Temperature



Maximum Safe Operating Area



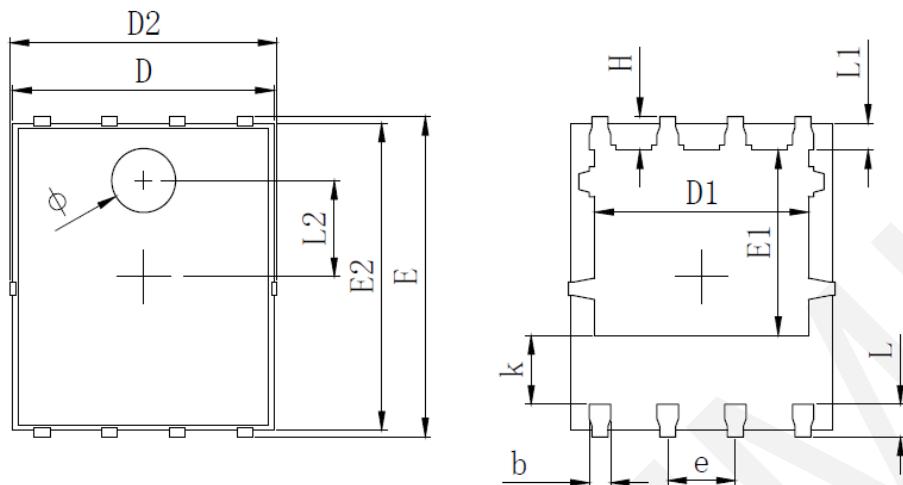
Single Pulse Power Rating, Junction-to-Case



Normalized Maximum Transient
Thermal Impedance



8. Package Dimensions



SYMBOL	MILLIMETER		
	MIN	Typ.	MAX
A	0.900	1.000	1.100
A1		0.254 REF.	
A2		0~0.05	
D	4.824	4.900	4.976
D1	3.910	4.010	4.110
D2	4.924	5.000	5.076
E	5.924	6.000	6.076
E1	3.375	3.475	3.575
E2	5.674	5.750	5.826
b	0.350	0.400	0.450
e		1.270 TYP.	
L	0.534	0.610	0.686
L1	0.424	0.500	0.576
L2		1.800 REF.	
k	1.190	1.290	1.390
H	0.549	0.625	0.701
θ	8°	10°	12°
ϕ	1.100	1.200	1.300
d			0.100

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