

XG0032

N-Channel Enhancement Mode SGT MOSFET

80V(VDS) / ± 20 V(VGS) / 120A(ID)

Rev0.1

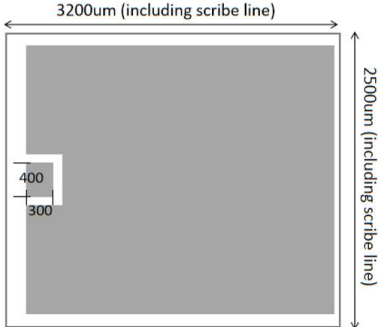
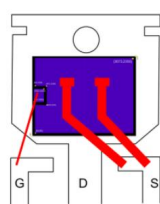


Wafer Specification

1. Order Information

Part No.	Description	Gross die / per 8" wafer
XG0032	80V(V_{DS}) / $\pm 20V(V_{GS})$/ 120A(I_D) N-Channel Enhancement Mode SGT MOSFET	3,601

2. Physical Characteristics

	Die Size: 2500 μ m $\times$ 3200 μ m (including scribe line)
	Gate: 300 μ m $\times$ 400 μ m
	For TO-220C-3L package Suggest Bonding wire: Gate: 1 $\times$ 5 mil AL Source: 2 $\times$ 15 mil AL 

3. Mechanical Data

Nominal Back Metal Composition, Thickness	Ti-Ni-Ag (13kA°)
Nominal Front Metal Composition, Thickness	AlCu (4 μ m)
Wafer Diameter	200mm, with 010 notch
Wafer Thickness	200 μ m
Scribe Line Width	60 μ m

4. Key Electrical Characteristics of Die (at $T_J = 25^\circ\text{C}$, unless otherwise specified)S

Parameter	Description	Min.	Typ.	Max.	Unit	Test Conditions
V _{(BR)DSS}	Drain-to-Source Breakdown Voltage	80			V	V _{GS} =0V, I _D =250μA
I _D (Device Ref)	Continuous Drain Current			120	A	T _J =25°C
R _{DS(on)} (FT)	Static Drain-to-Source On-Resistance		4.4	5.5	mΩ	V _{GS} =10V, I _D =20A
V _{GS(th)}	Gate Threshold Voltage	2.0	3.0	4.0	V	V _{DS} =V _{GS} , I _D =250μA
I _{DSS}	Drain-to-Source Leakage Current			1	μA	V _{DS} =80V, V _{GS} =0V
I _{GSS}	Gate-to-Source Leakage Current			±100	nA	V _{GS} =±20V, V _{DS} =0V
R _g	Gate Poly Resistance	1	2.1	4	Ω	V _{DS} =0V, V _{GS} =0, f=1MHZ
C _{iss}	Input Capacitance		3650		pF	V _{DS} =40V, V _{GS} =0, f=1MHZ
C _{oss}	Output Capacitance		1870		pF	
C _{rss}	Reverse Transfer Capacitance		120		pF	
T _J , T _{STG}	Operating and Storage Temperature	-55°C to 150°C Max				