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万代半导体

WP3415SS

Enhancement Mode P-Channel Power MOSFET

SOT23/PMOS/-20V/±12V/-0.8V/-4.1A/30mΩ

Rev1.0

-20V, 30mΩ, -4.1A, P-Channel MOSFET

1.Features

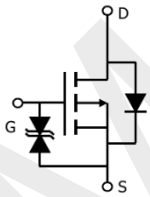
- Advanced Trench Technology
- Surface mount package
- ESD Protected

V_{DS}	$R_{DS(on)}$ Typ.	I_D.
-20V	30mΩ @ -4.5V	-4.1A
	38mΩ @ -2.5V	

2.Applications

- Power Management
- Load Switching

SOT23
Pin Description



Schematic Diagram

3.Package Marking and Ordering Information

Part no.	Marking	Package	PCS/Reel	PCS/CTN.
WP3415SS	AF4E 印	SOT23	3,000	180,000

4.Absolute Max Ratings at Ta=25℃ (Note1)

Parameter	Symbol	Maximum	Units
Drain to Source Voltage	V_{DSS}	-20	V
Gate to Source Voltage	V_{GSS}	±12	V
Drain Current (DC)	I_D	-4.1	A
Drain Current (Pulse), PW≤300μs	I_{DP}	-16.4	A
Total Dissipation	P_D	1.1	W
Junction Temperature	T_j	150	℃
Storage Temperature	T_{stg}	-55 to +150	℃

Note 1: Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

5.Thermal Resistance Ratings (Note 2)

Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	114	℃/W

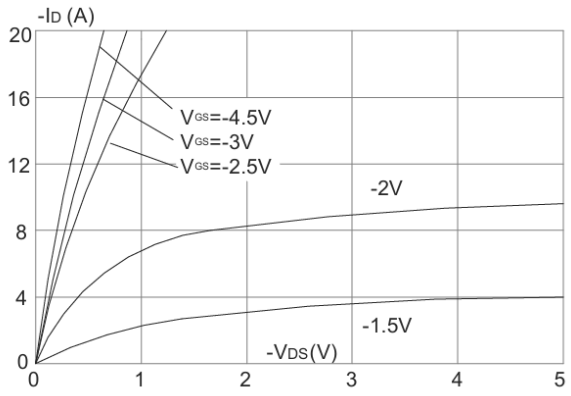
Note 2: When mounted on 1 inch square copper board t ≤ 10sec The value in any given application depends on the user's specific board design.

6.Electrical Characteristics at Ta=25℃ (Note 3)

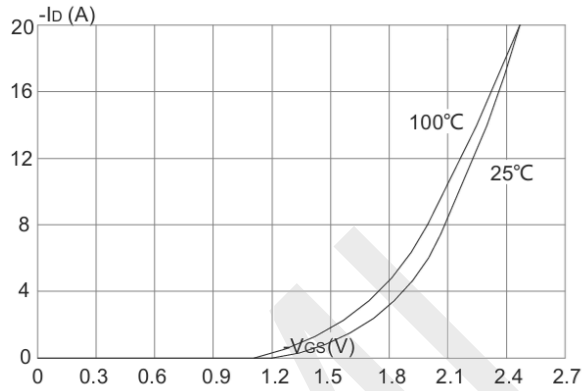
Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Units
Drain to Source Breakdown Voltage	$V_{(BR)DSS}$	I_D = -250μA, V_{GS} = 0V	-20	-	-	V
Zero-Gate Voltage Drain Current	I_{DSS}	V_{DS} = -20V, V_{GS} = 0V	-	-	-1	uA
Gate to Source Leakage Current	I_{GSS}	V_{GS} = ±12V, V_{DS} = 0V	-	-	±10	uA
Gate Threshold Voltage	$V_{GS(th)}$	V_{DS}=V_{GS}, I_{DS}=-250μA	-0.4	-0.8	-1.1	V
Static Drain to Source On-State Resistance	$R_{DS(on)}$	I_D =-4.1A, V_{GS} =-4.5V	-	30	37	mΩ
		I_D =-3A, V_{GS} = -2.5V	-	38	50	mΩ
Input Capacitance	C_{iss}	V_{GS}=0V, V_{DS}=-10V, Frequency=1.0MHz	-	289	-	pF
Output Capacitance	C_{oss}		-	98	-	pF
Reverse Transfer Capacitance	C_{rss}		-	22	-	pF
Turn-ON Delay Time	$t_{d(on)}$	V_{DD} =-10V, R_L=1.2Ω, R_G = 1Ω, V_{GS} = -4.5V	-	12	-	ns
Rise Time	t_r		-	35	-	ns
Turn-OFF Delay Time	$t_{d(off)}$		-	30	-	ns
Fall Time	t_f		-	10	-	ns
Total Gate Charge	Q_g	V_{DS} = -10V, V_{GS} = -4.5V, I_D = -4.1A	-	9	-	nC
	Q_{gs}		-	1	-	nC
	Q_{gd}		-	2.6	-	nC
Diode Forward Voltage	V_{FSD}	I_{SD} = -4.1A, V_{GS} = 0	-0.5	-	-1.2	V

Note 3: Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

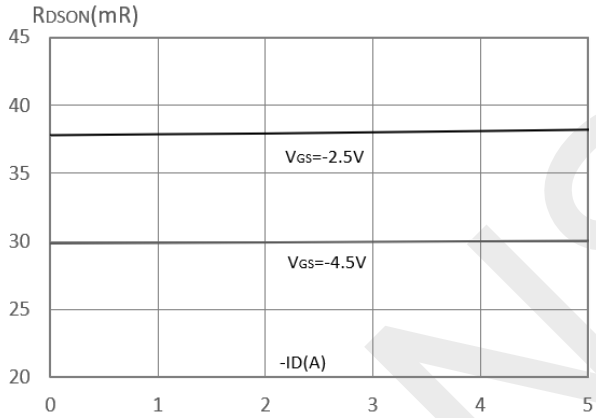
7.Typical Electrical and Thermal Characteristics



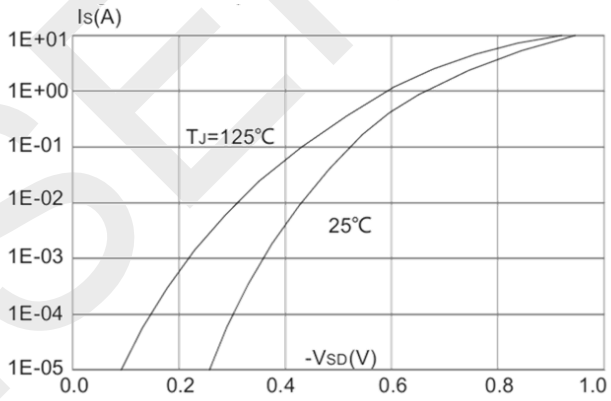
Output Characteristics



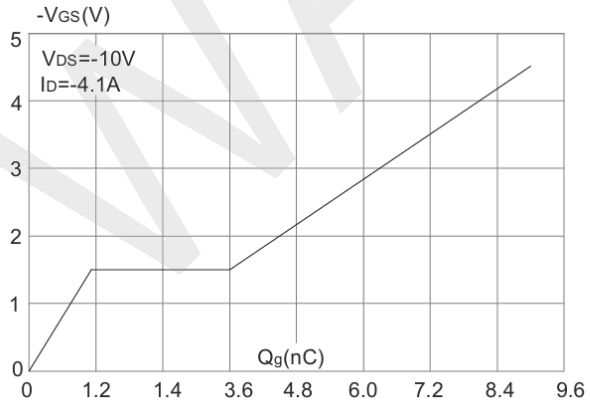
Typical Transfer Characteristics



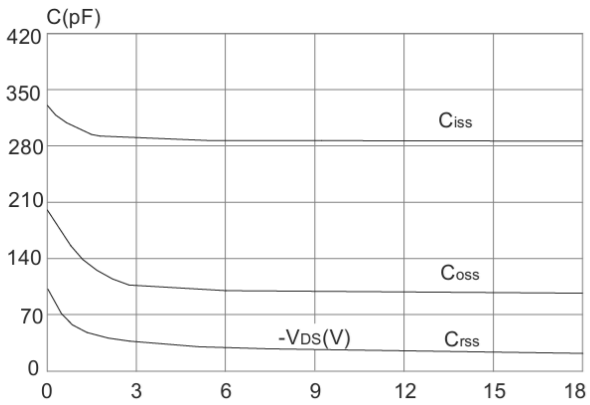
On-resistance vs. Drain Current



Body Diode Characteristics



Gate Charge Characteristics



Capacitance Characteristics

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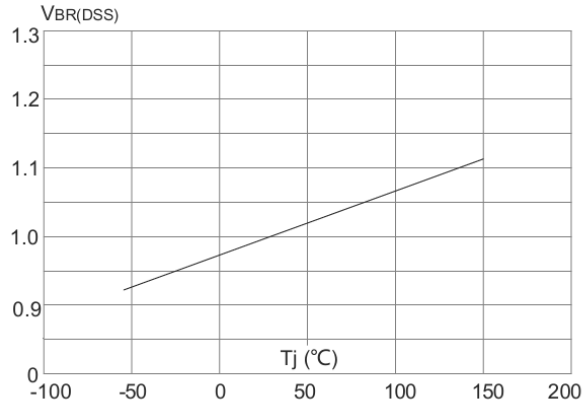
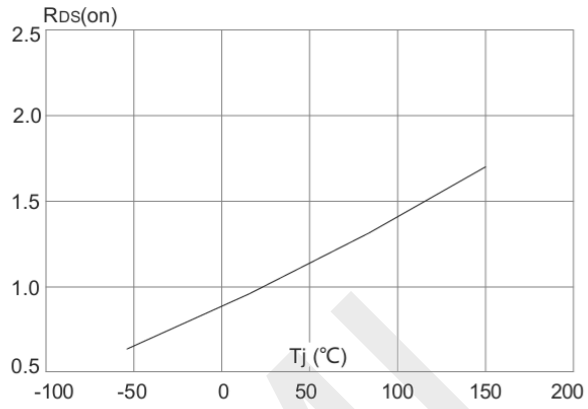
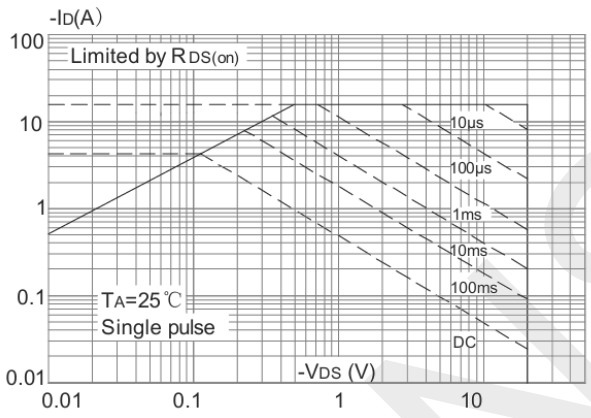


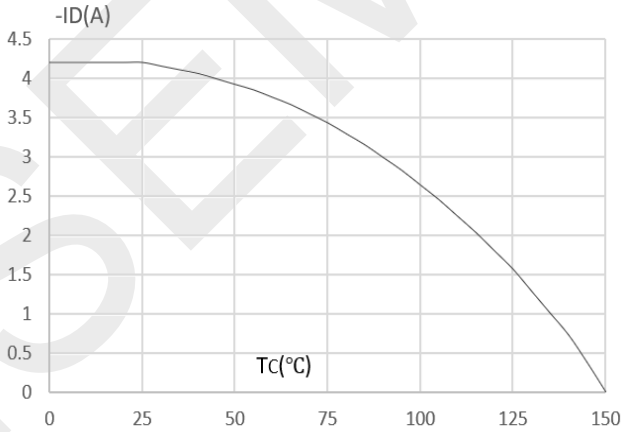
Normalized Breakdown Voltage vs. Junction Temperature



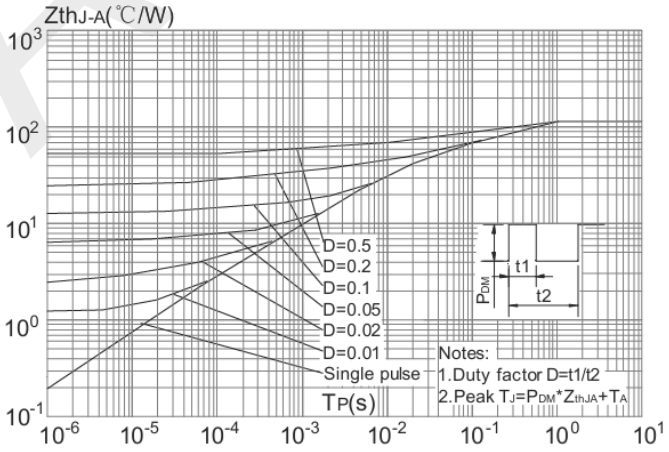
Normalized on Resistance vs.Junction Temperature



Maximum Safe Operating Area

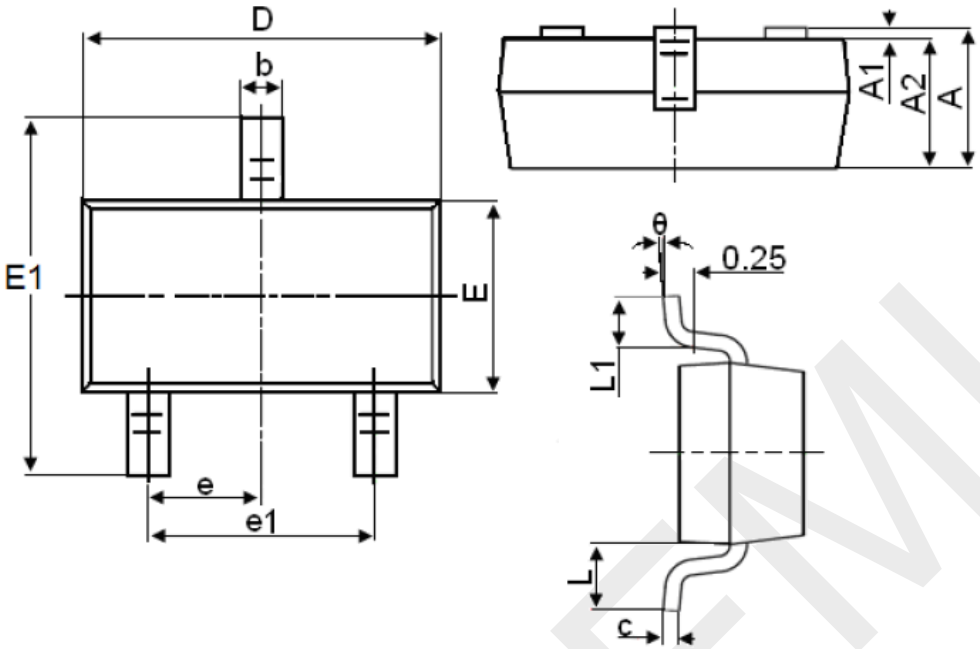


Maximum Continuous Drain Current vs. Ambient Temperature



Maximum Effective Transient Thermal Impedance, Junction-to-Ambient

8.Package Dimensions



Symbol	Dimensions in Millimeters		
	MIN.	TYP.	MAX.
A	0.900		1.150
A1	0.000		0.100
A2	0.900		1.050
b	0.300		0.500
c	0.080		0.150
D	2.800		3.000
E	1.200		1.400
E1	2.250		2.550
e		0.950	
e1	1.800		2.000
L		0.550	
L1	0.300		0.500
θ	0°		8°

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