WANSEMI
万代半导体

WP4080K

Enhancement Mode N-Channel Power MOSFET

TO-252/NMOS/40V/±20V/1.6V/80A/5.9mΩ

Rev1.0

40V, 5.9mΩ, 80A, Single N-Channel

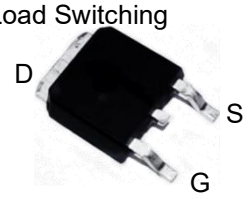
1.Features

- 40V MOSFET technology
- Low on-state resistance
- Fast switching
- Vgs±20V
- 100% RG Tested
- 100% UIS Tested

V_{DS}	$R_{DS(on)}$ Typ.	I_D Max.
40V	5.9mΩ @ 10V	80A
	7.2mΩ @ 4.5V	

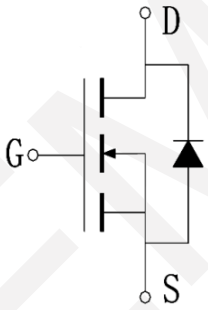
2.Applications

- Power Switching Application
- Load Switching



TO-252

Pin Description



Schematic Diagram

3.Package Marking and Ordering Information

Part no.	Marking	Package	PCS/Reel	PCS/CTN.
WP4080K	WP4080K	TO-252	2,500	25,000

4.Absolute Max Ratings at Ta=25℃ (Note1)

Parameter	Symbol	Maximum	Units
Drain to Source Voltage	V_{DSS}	40	V
Gate to Source Voltage	V_{GSS}	±20	V
Drain Current (DC)	I_D	80	A
Drain Current (Pulse), PW≤300μs	I_{DP}	320	A
Total Dissipation	P_D	80	W
Avalanche Energy, Single Pulsed	E_{AS}	156	mJ
Junction Temperature	T_j	150	℃
Storage Temperature	T_{stg}	-55 to +150	℃

Note 1: Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

5.Thermal Resistance Ratings

Parameter	Symbol	Value	Unit
Thermal Resistance, Junction to Case	$R_{\theta JC}$	2.8	℃/W

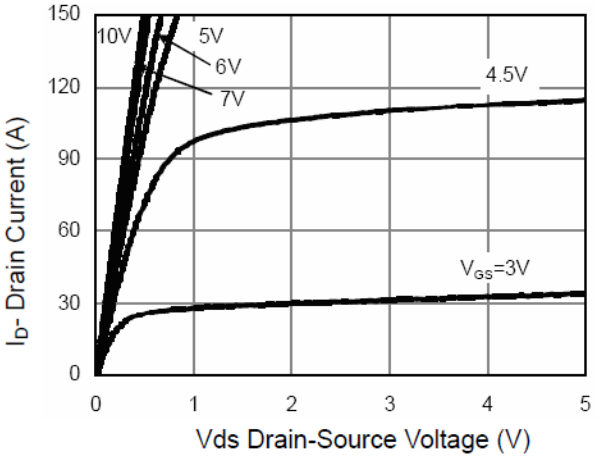
Note 2：When mounted on 1 inch square copper board t ≤ 10sec The value in any given application depends on the user's specific board design.

6.Electrical Characteristics at Ta=25℃ (Note 3)

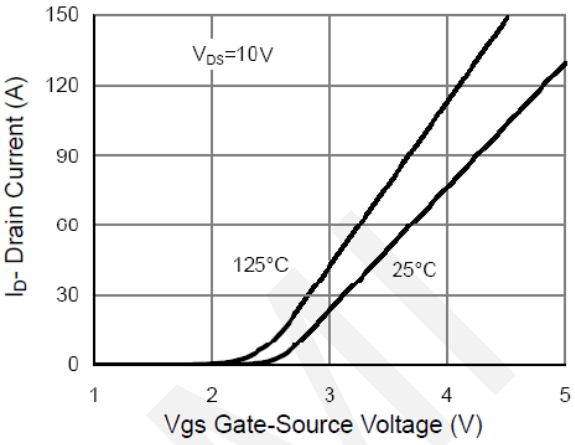
Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Units
Drain to Source Breakdown Voltage	$V_{(BR)DSS}$	I_D = 250μA, V_{GS} = 0V	40	-	-	V
Zero-Gate Voltage Drain Current	I_{DSS}	V_{DS} = 40V, V_{GS} = 0V	-	-	1	μA
Gate to Source Leakage Current	I_{GSS}	V_{GS} = ±20V, V_{DS} = 0V	-	-	±100	nA
Gate Threshold Voltage	$V_{GS(th)}$	V_{DS}=V_{GS}, I_{DS}=250μA	1.0	1.6	2.5	V
Static Drain to Source On-State Resistance	$R_{DS(on)}$	I_D = 20A, V_{GS} = 10V	-	5.9	7.2	mΩ
		I_D = 10A, V_{GS} = 4.5V	-	7.2	10	mΩ
Input Capacitance	C_{iss}	V_{GS}=0V, V_{DS}=20V, Frequency=1.0MHz	-	2242	-	pF
Output Capacitance	C_{oss}		-	163	-	pF
Reverse Transfer Capacitance	C_{rss}		-	136	-	pF
Turn-ON Delay Time	$t_{d(on)}$	V_{DD} = 20V, R_L = 1Ω V_{GS} = 10V, R_G = 3Ω	-	12	-	ns
Rise Time	t_r		-	11	-	ns
Turn-OFF Delay Time	$t_{d(off)}$		-	39	-	ns
Fall Time	t_f		-	12	-	ns
Total Gate Charge	Q_g	V_{DS} = 20V, V_{GS} = 10V, I_D = 20A	-	54.3	-	nC
	Q_{gs}		-	6.9	-	nC
	Q_{gd}		-	14.5	-	nC
Diode Forward Voltage	V_{FSD}	I_S = 10A, V_{GS} = 0	-	0.85	1.2	V

Note 3：Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

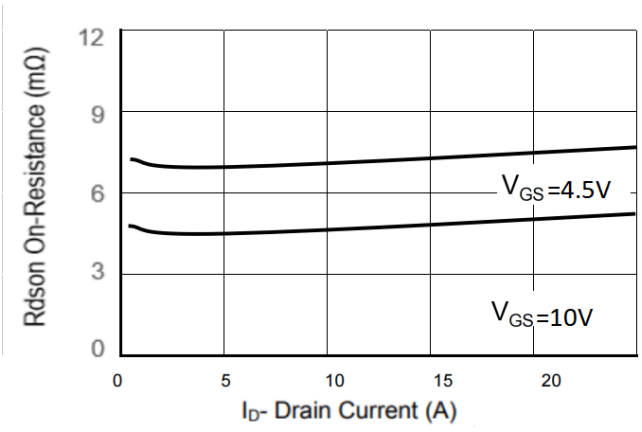
7.Typical electrical and thermal characteristics



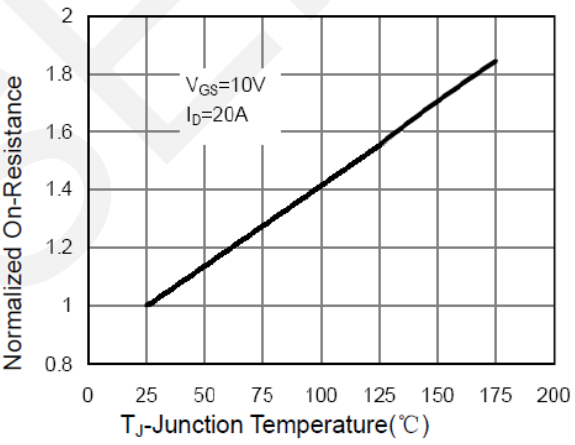
Output Characteristics



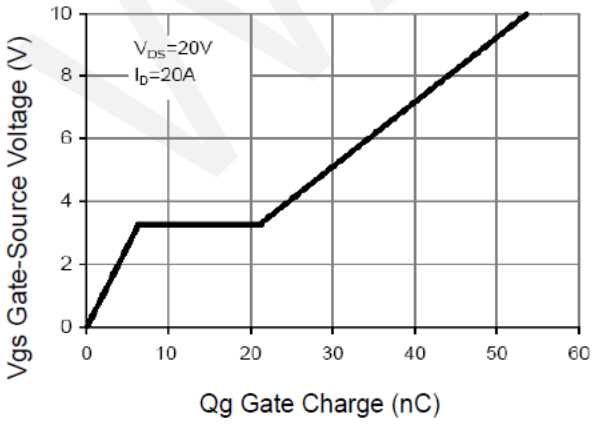
Transfer Characteristics



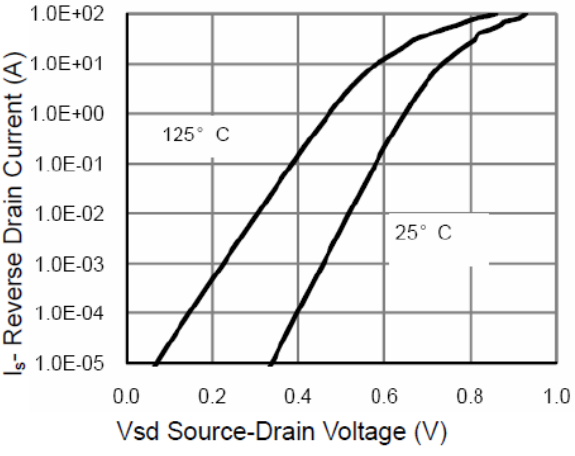
Rdson-Drain Current



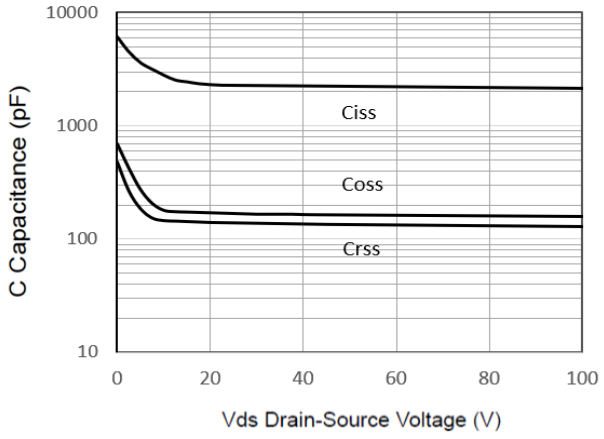
Rdson-Junction Temperature



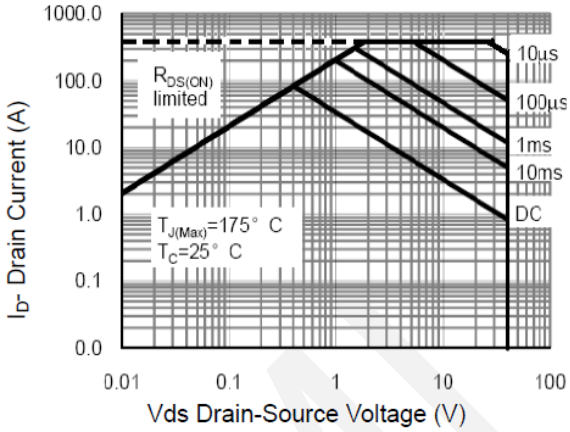
Gate Charge



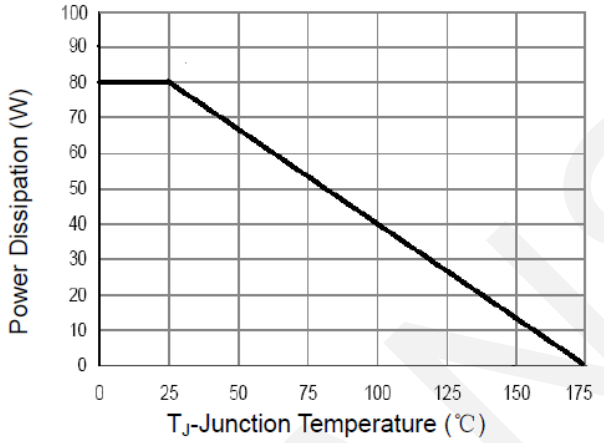
Source-Drain Diode Forward



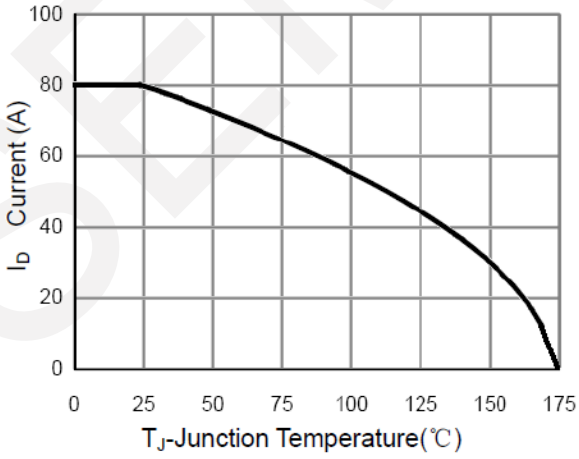
Capacitance vs Vds



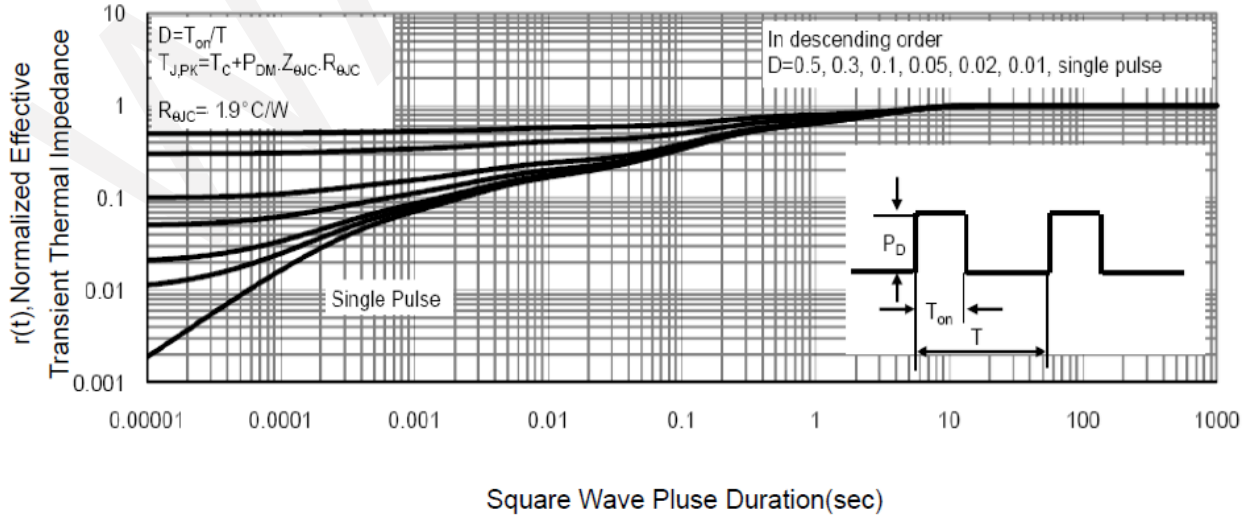
Safe Operation Area



Power De-rating

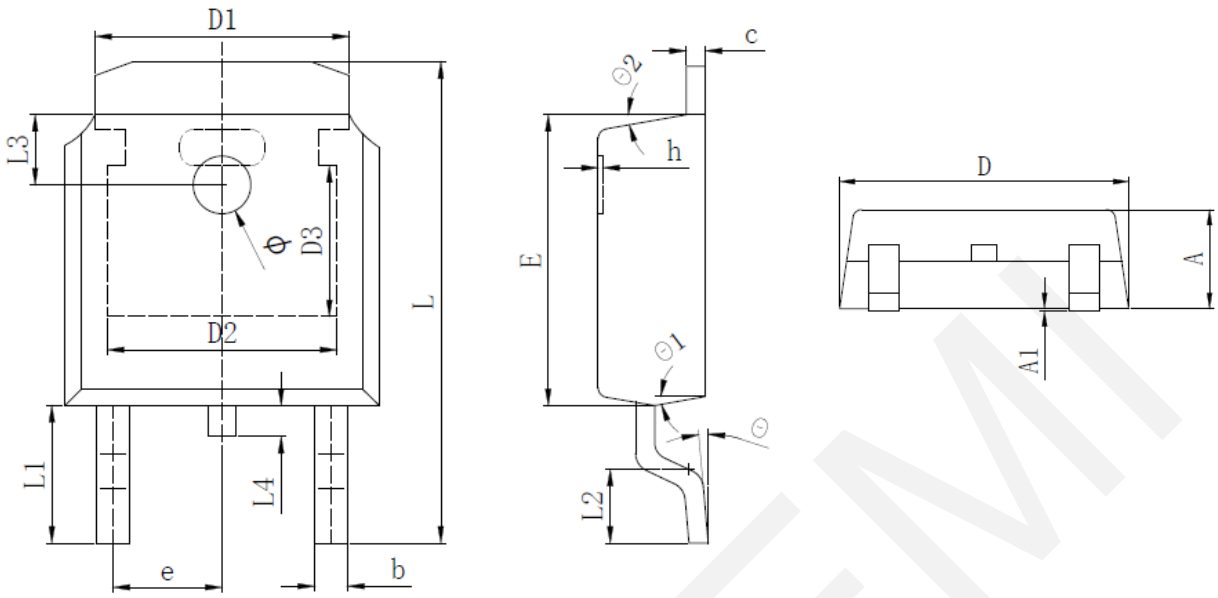


ID Current vs. Junction Temperature



Normalized Maximum Transient Thermal Impedance

8.Package Dimensions



SYMBOL	MILLIMETER		
	MIN	Typ.	MAX
A	2.200	2.300	2.400
A1	0.000		0.127
b	0.640	0.690	0.740
c(电镀后)	0.460	0.520	0.580
D	6.500	6.600	6.700
D1	5.334 REF		
D2	4.826 REF		
D3	3.166 REF		
E	6.000	6.100	6.200
e	2.286 TYP		
h	0.000	0.100	0.200
L	9.900	10.100	10.300
L1	2.888 REF		
L2	1.400	1.550	1.700
L3	1.600 REF		
L4	0.600	0.800	1.000
Φ	1.100	1.200	1.300
θ	0°		8°
θ1	9° TYP		
θ2	9° TYP		

9. Important Notice

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